

CWI

C W I L E C T U R E S

UNDERSTANDING AND
SIMPLIFYING
SOFTWARE

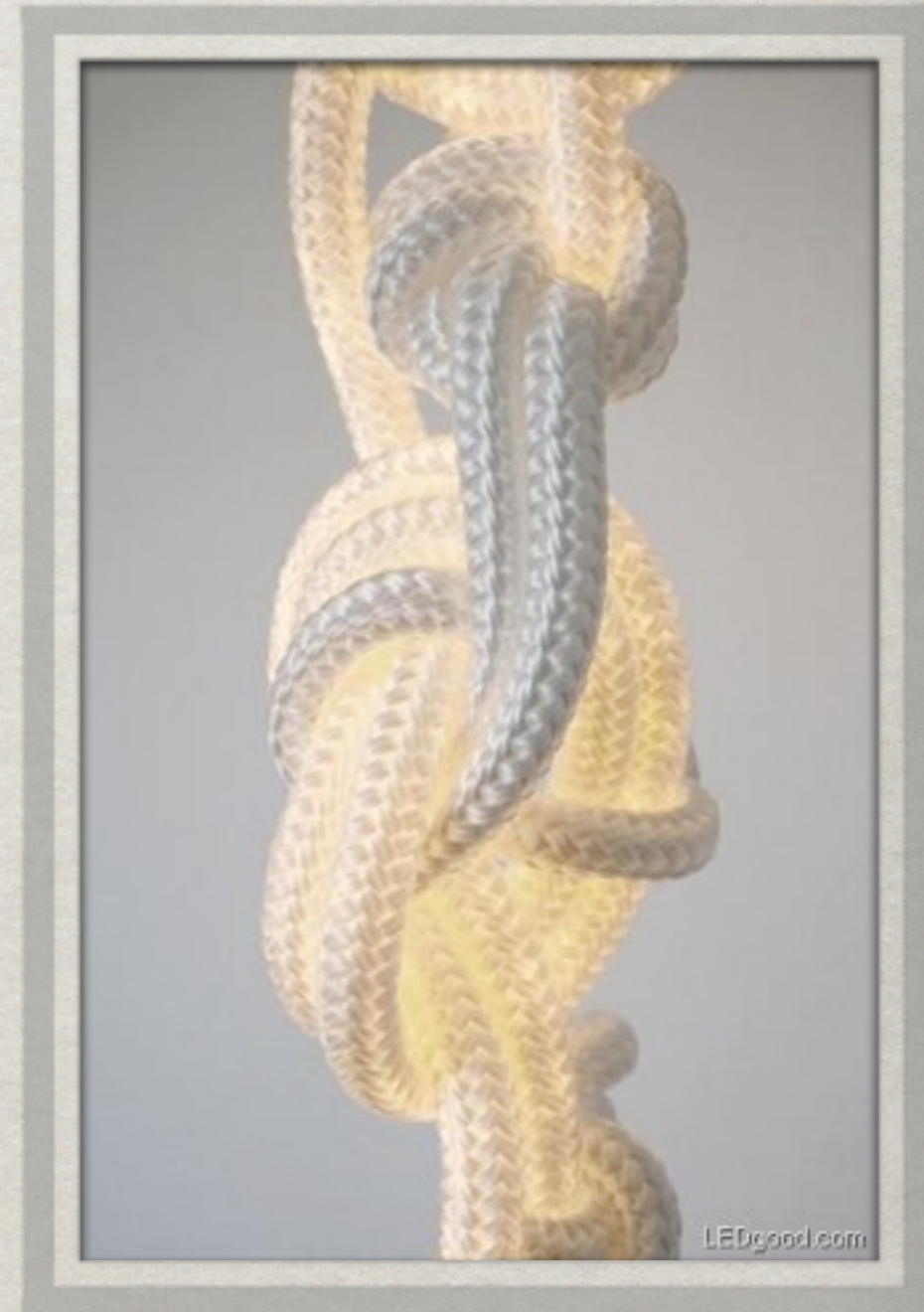
WITH THE **RASCAL**
META-PROGRAMMING
LANGUAGE

J U R G E N J . V I N J U



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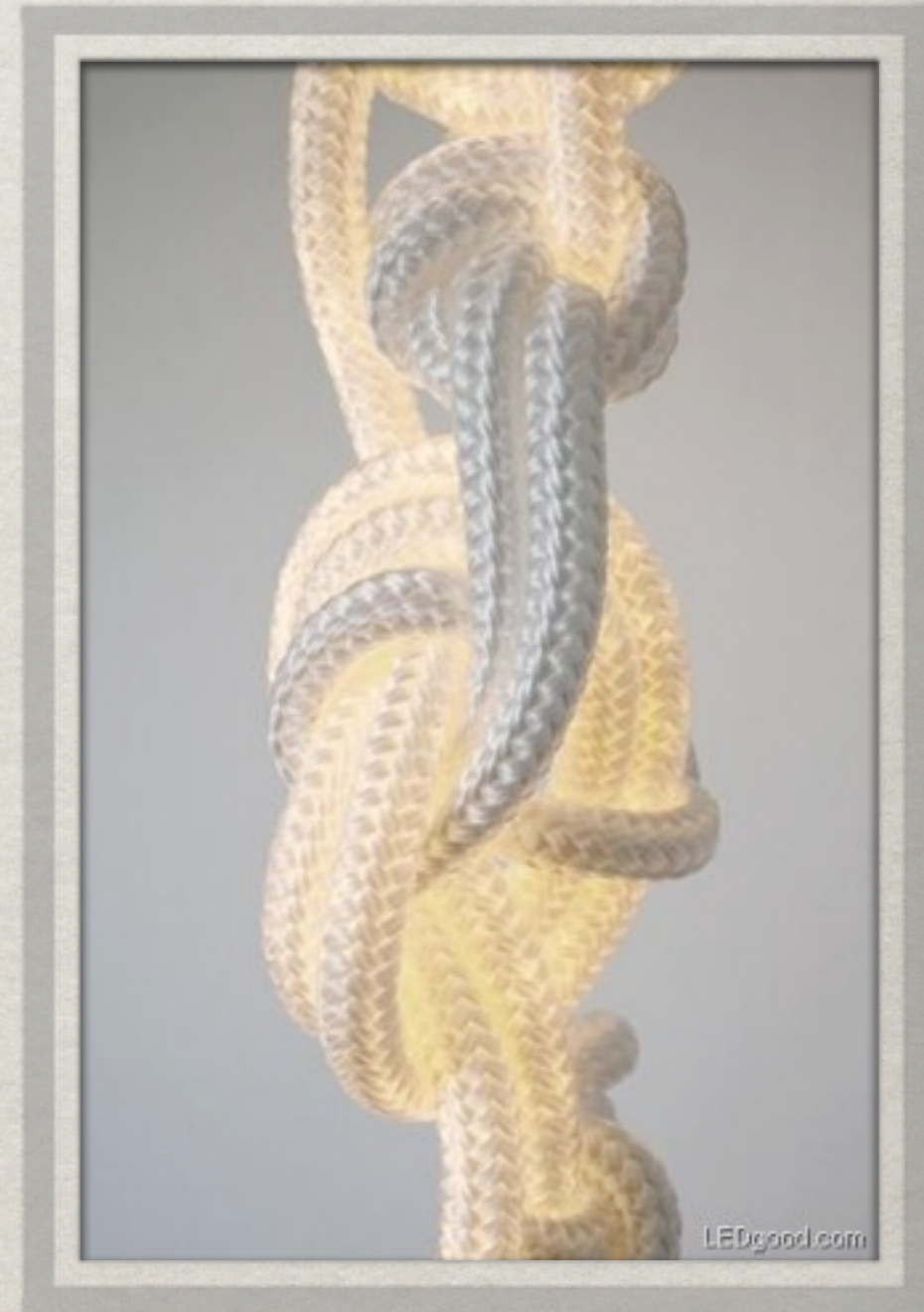
SOFTWARE IS COMPLEX



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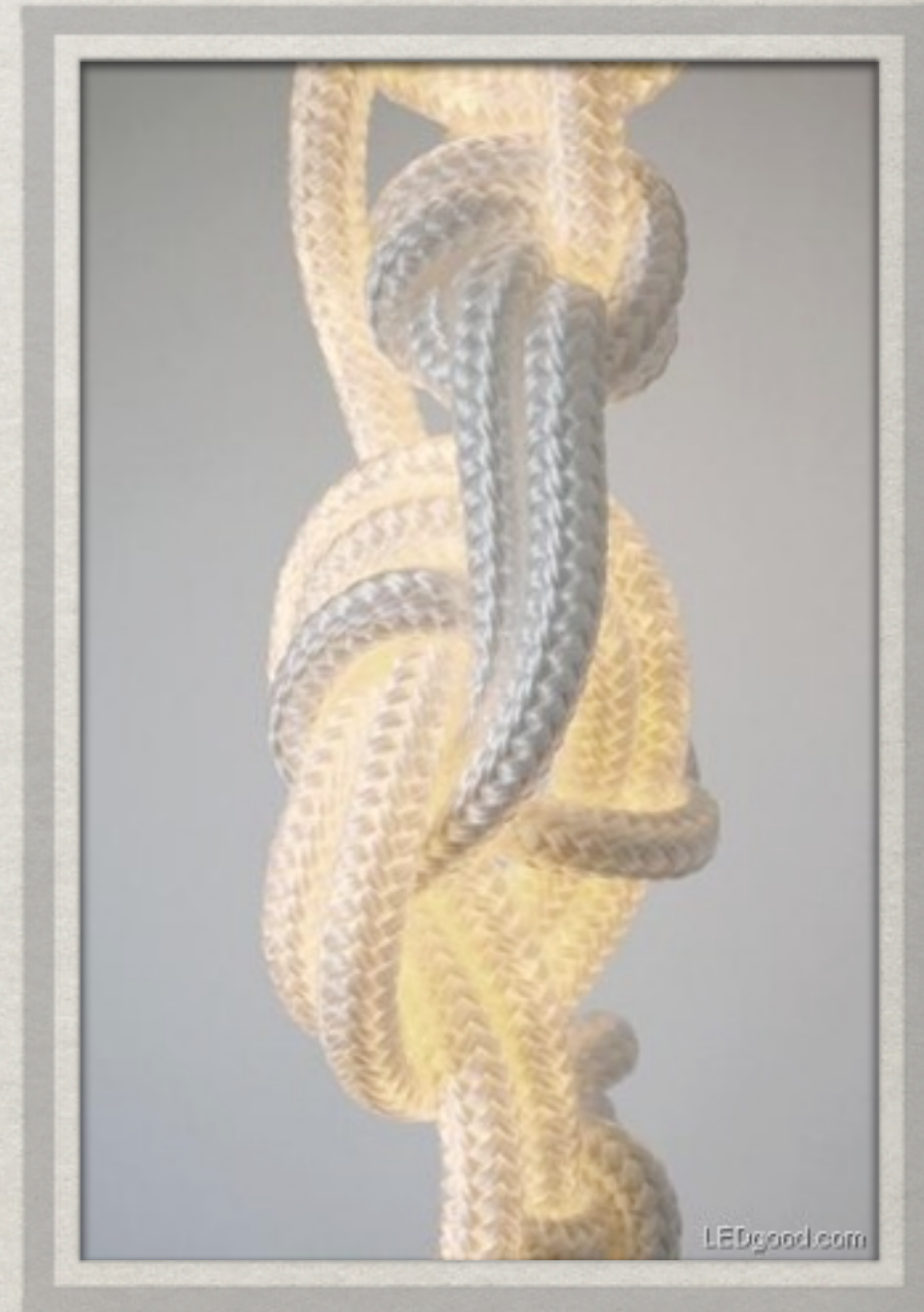
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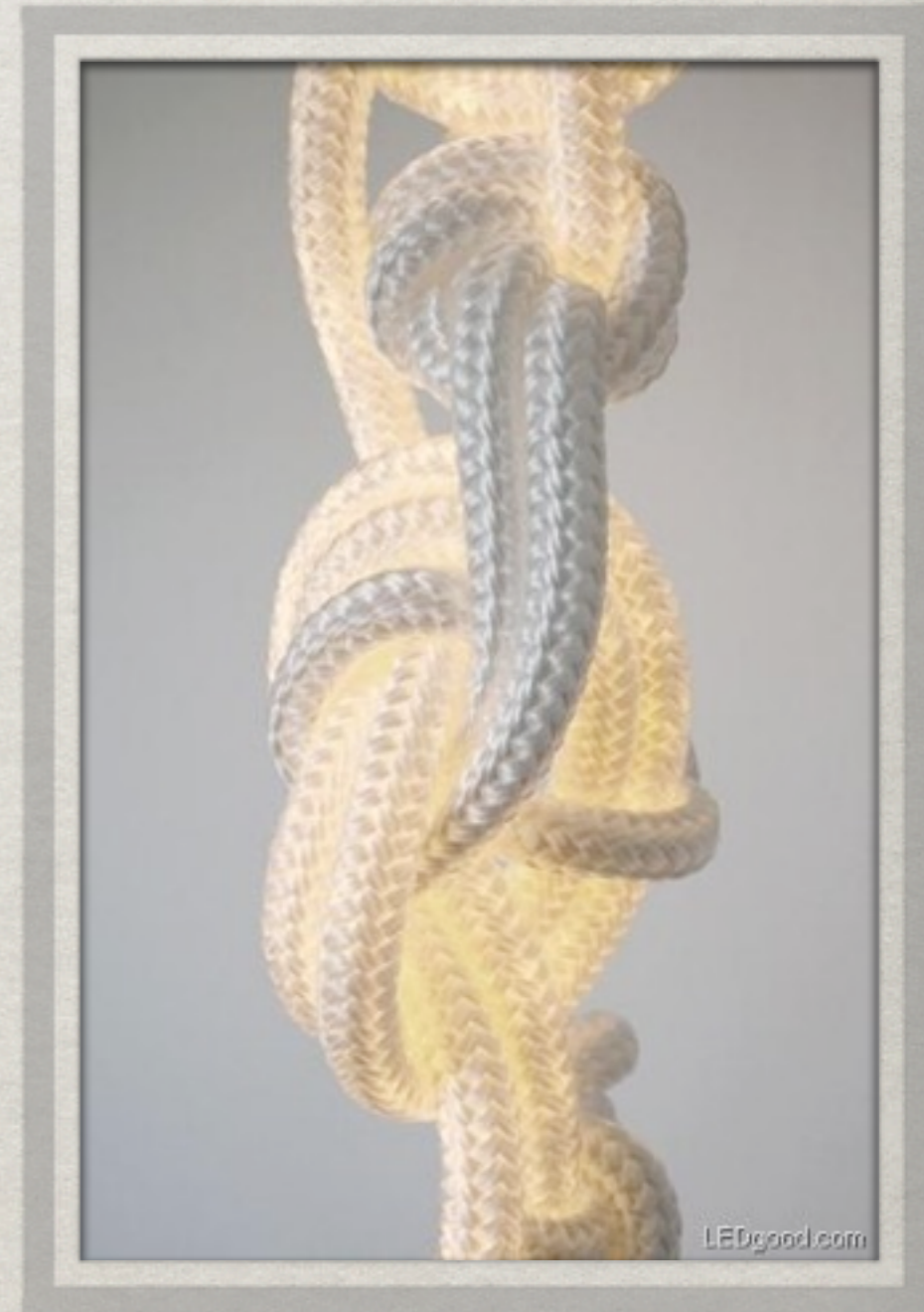
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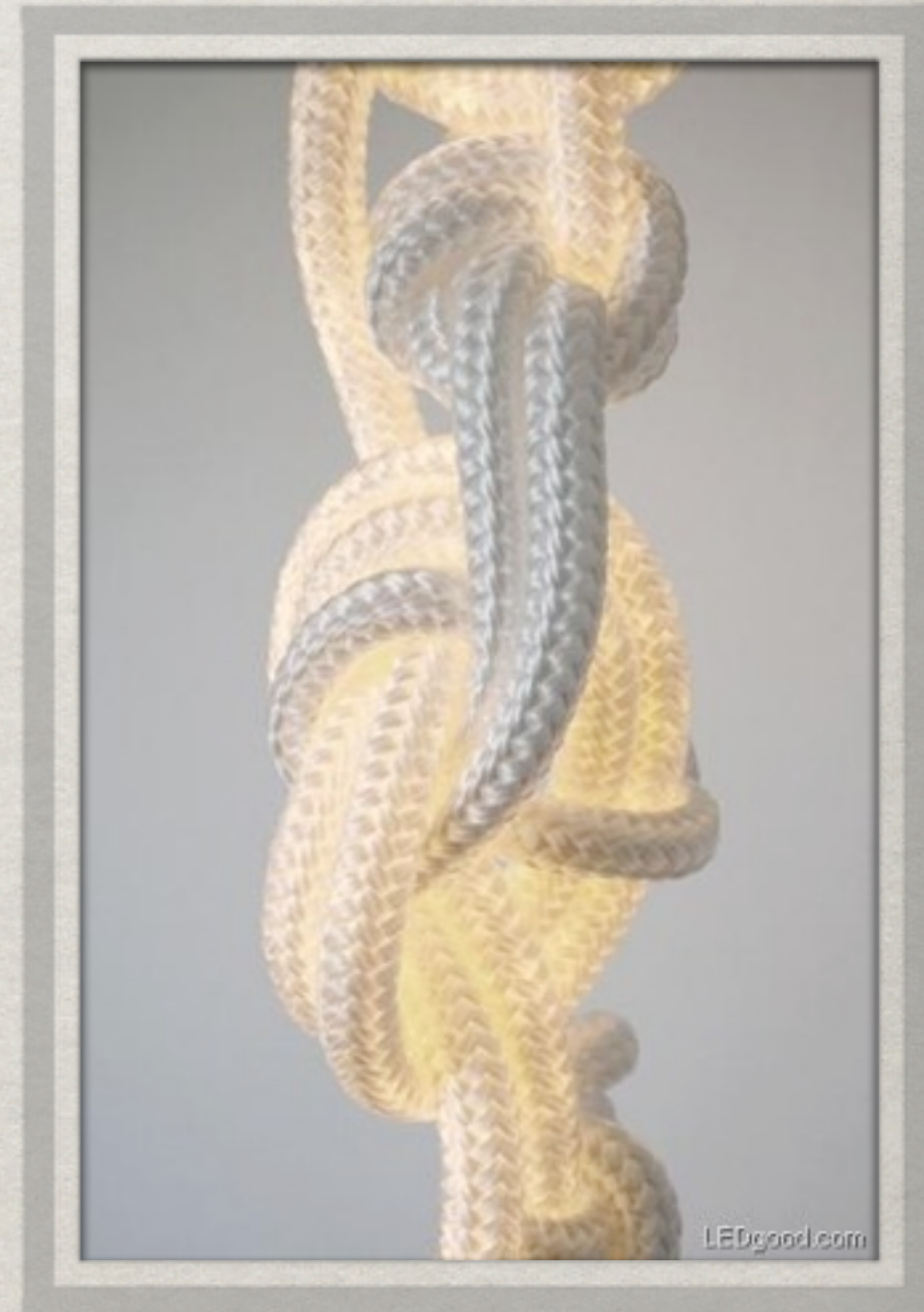
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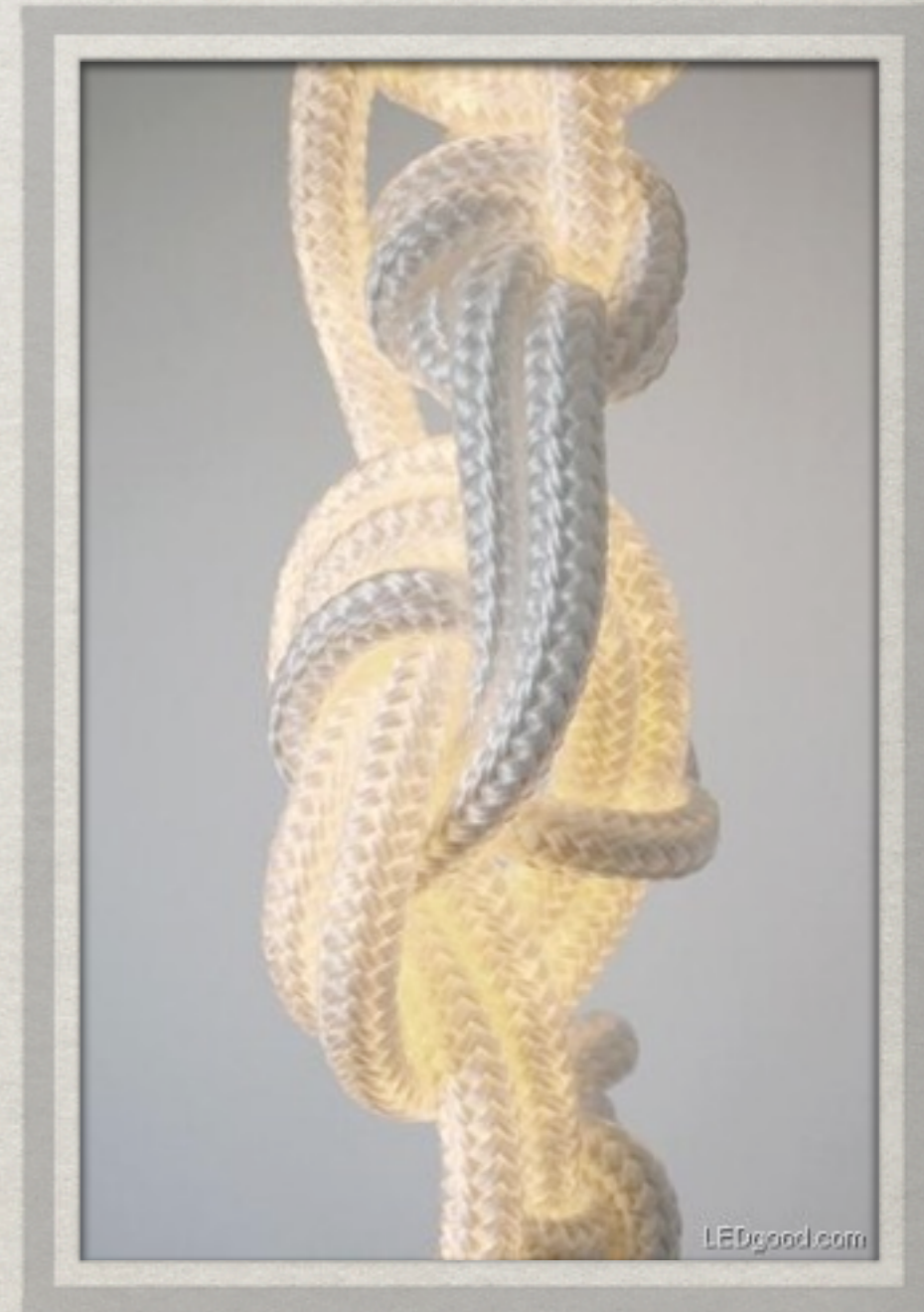
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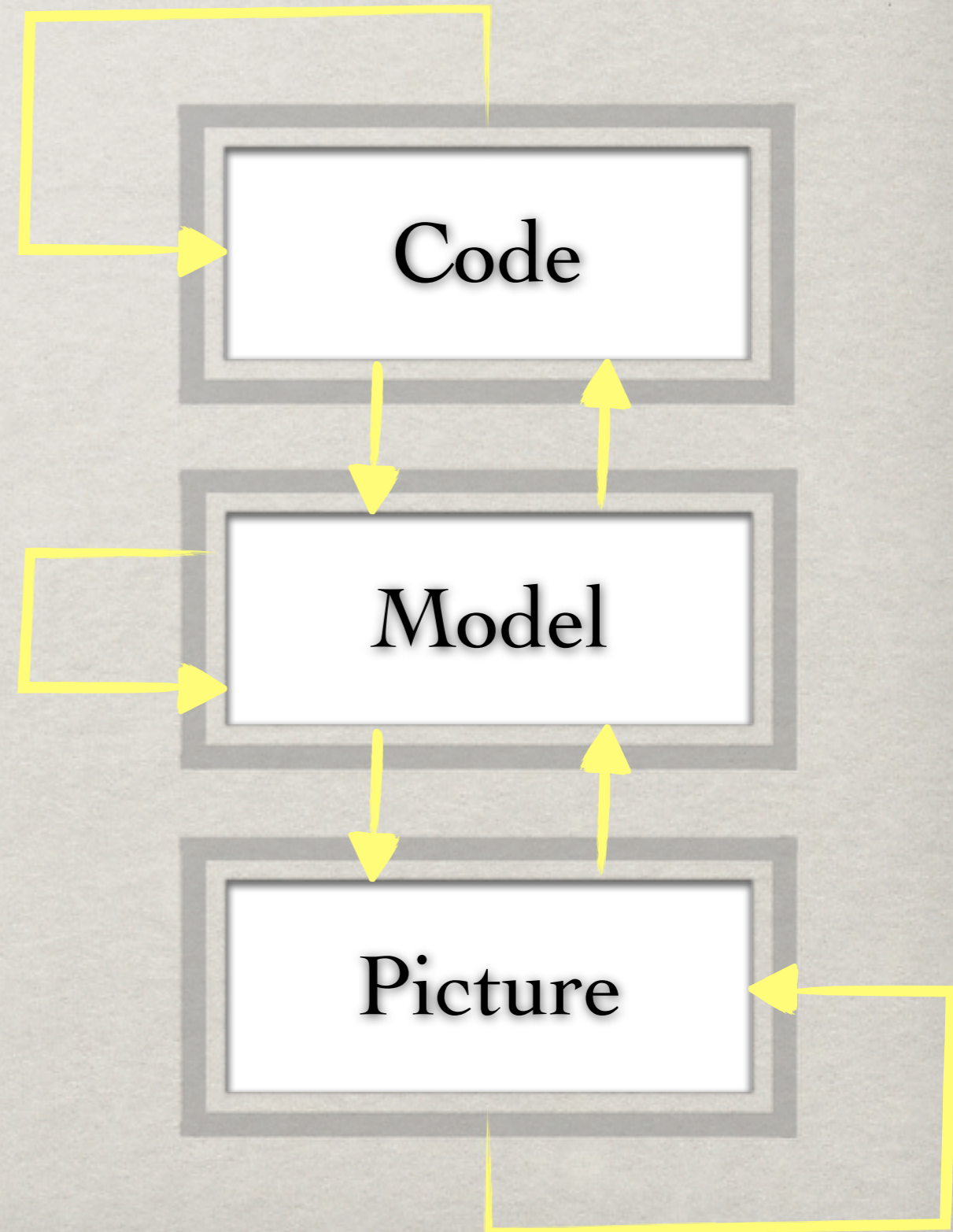
INSTRUMENTS

- ✻ Higher level programming
 - ✻ Model Driven Engineering
 - ✻ Domain Specific Languages

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PROBLEM

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- ☼ Instruments are costly
 - ☼ Diverse
 - ☼ Multi-disciplinary



PROBLEM

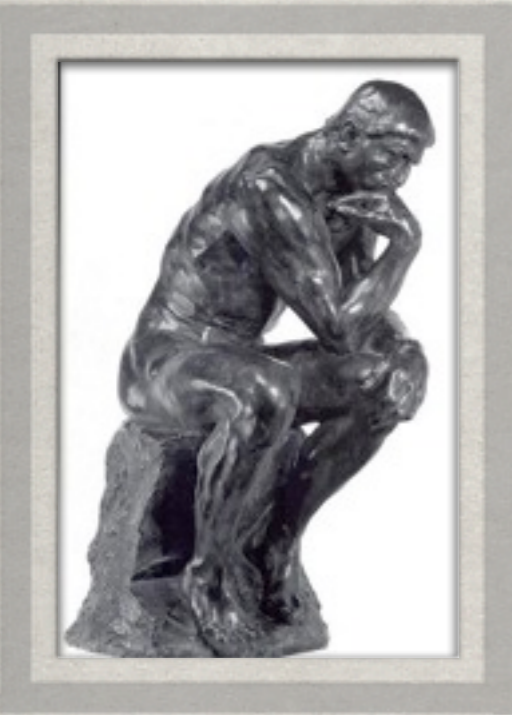
- ☼ Instruments are costly
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 - ☼ Multi-disciplinary
 - ☼ Accurate/Efficient



RASCAL



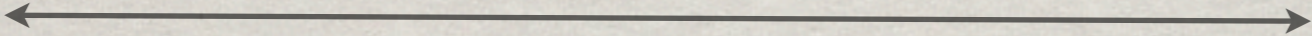
ENABLES



Research



Application

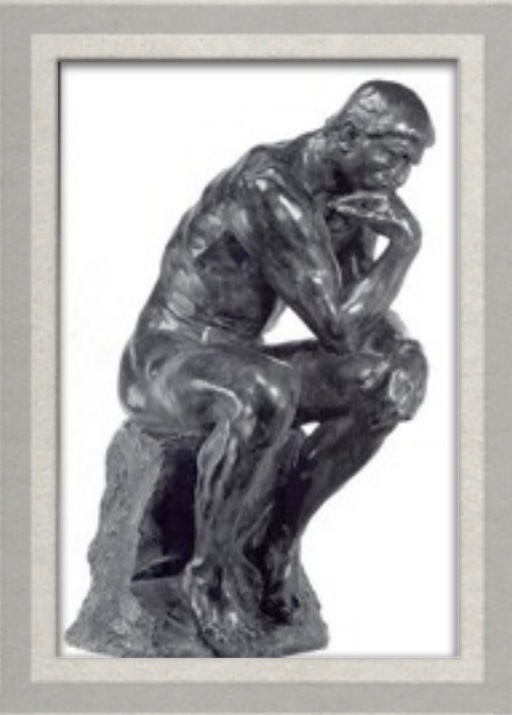


RASCAL



A new tool every week

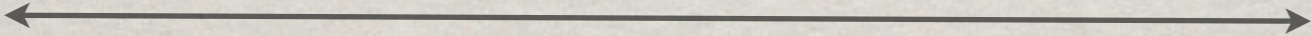
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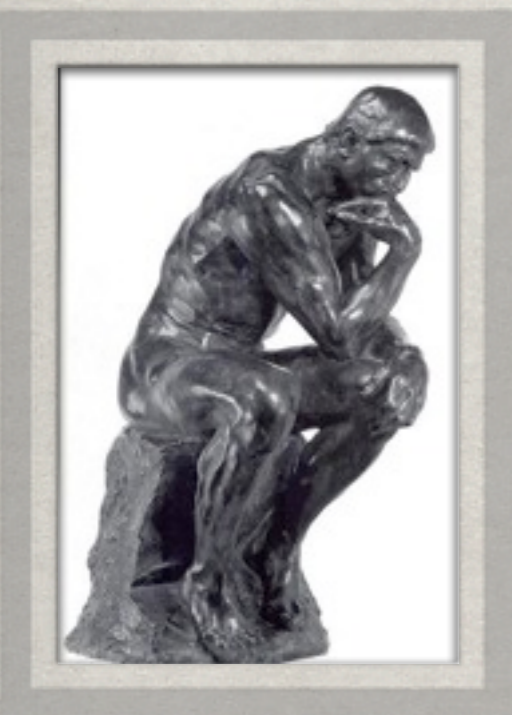
RASCAL

One-stop-shop

A new tool every week



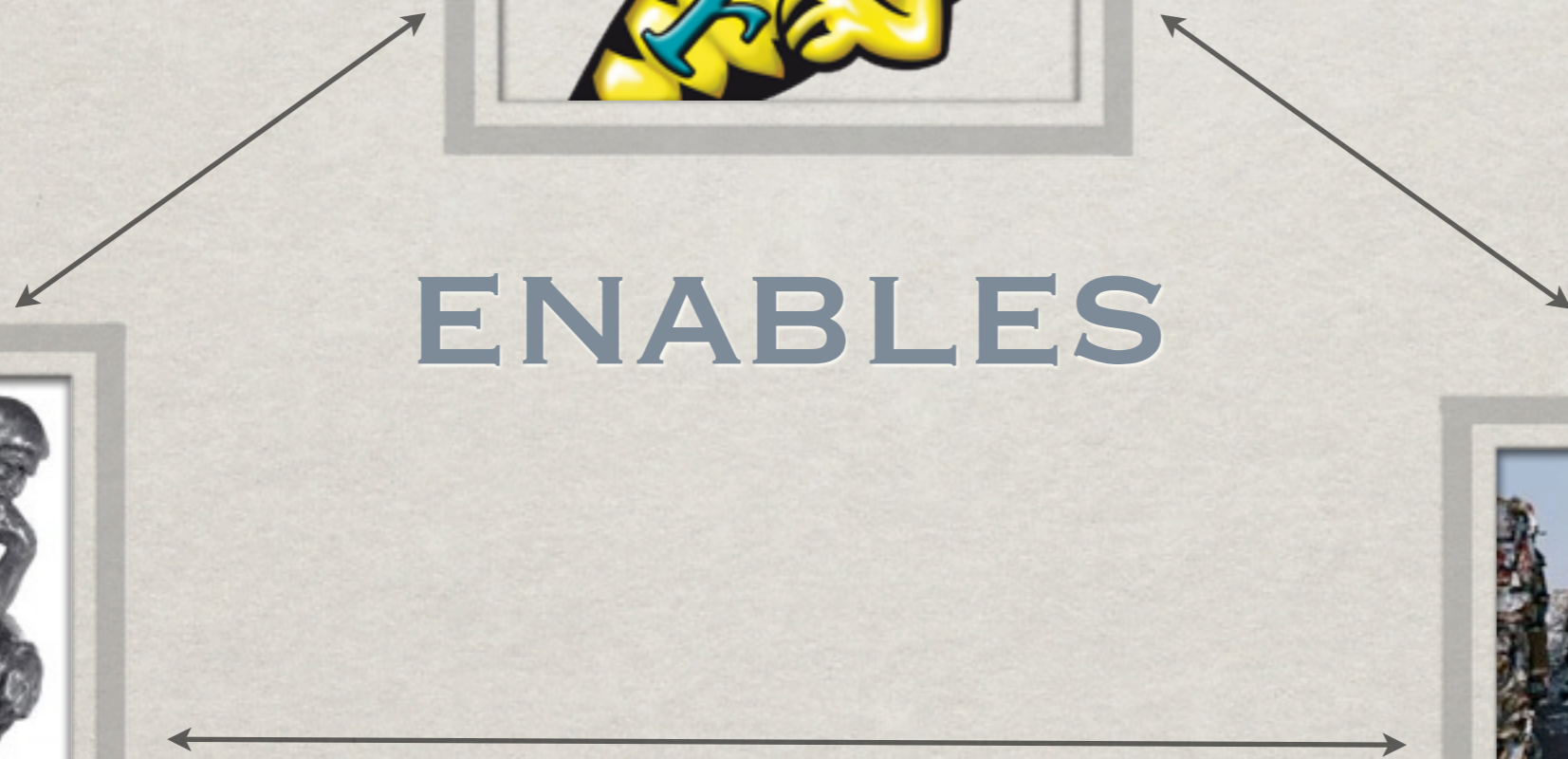
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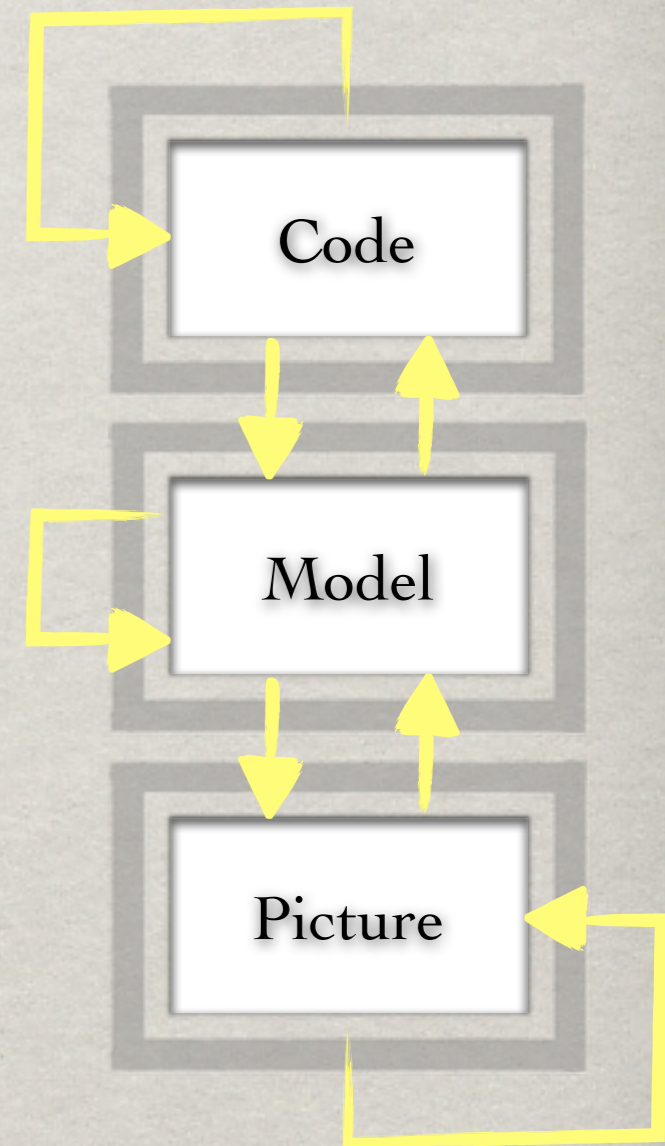
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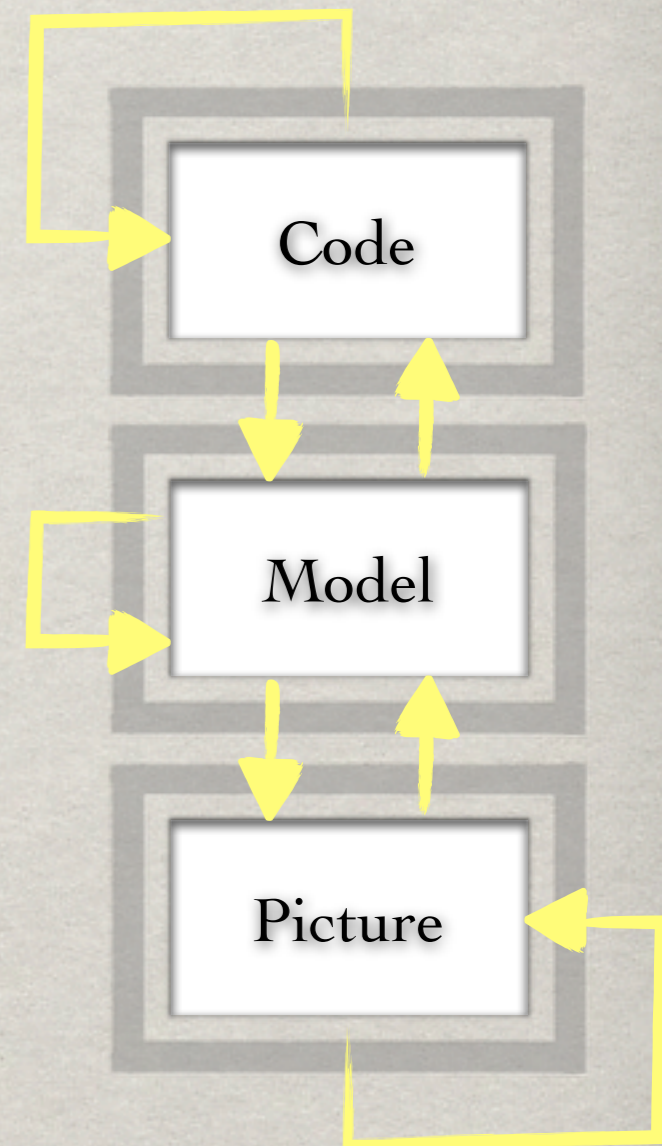


RASCAL IS EASY



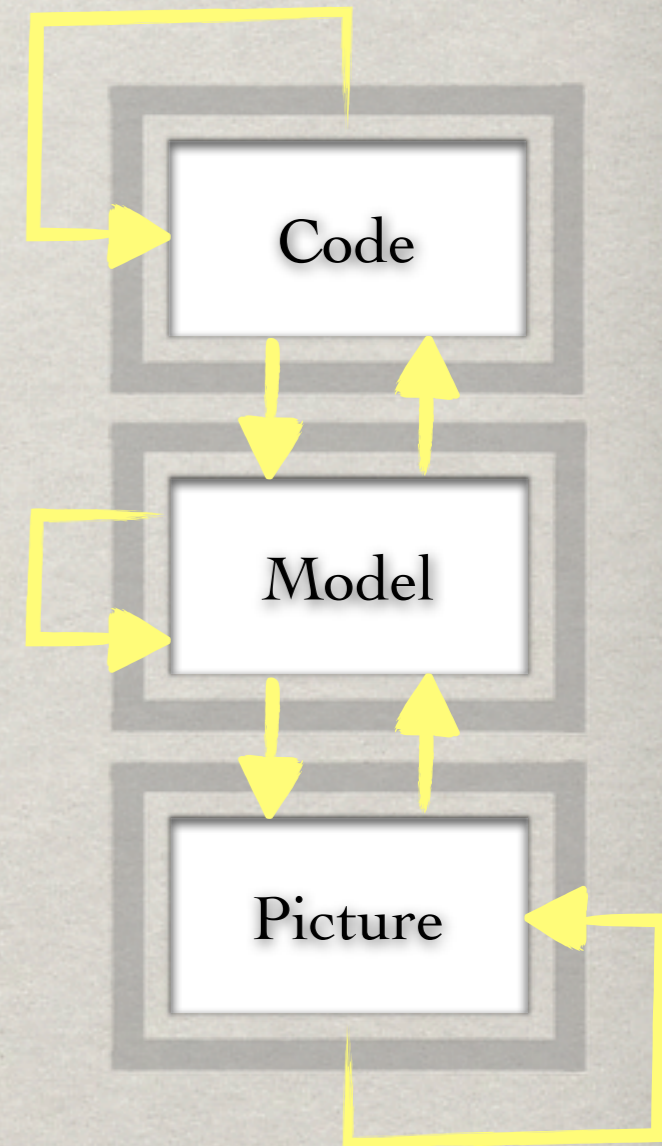
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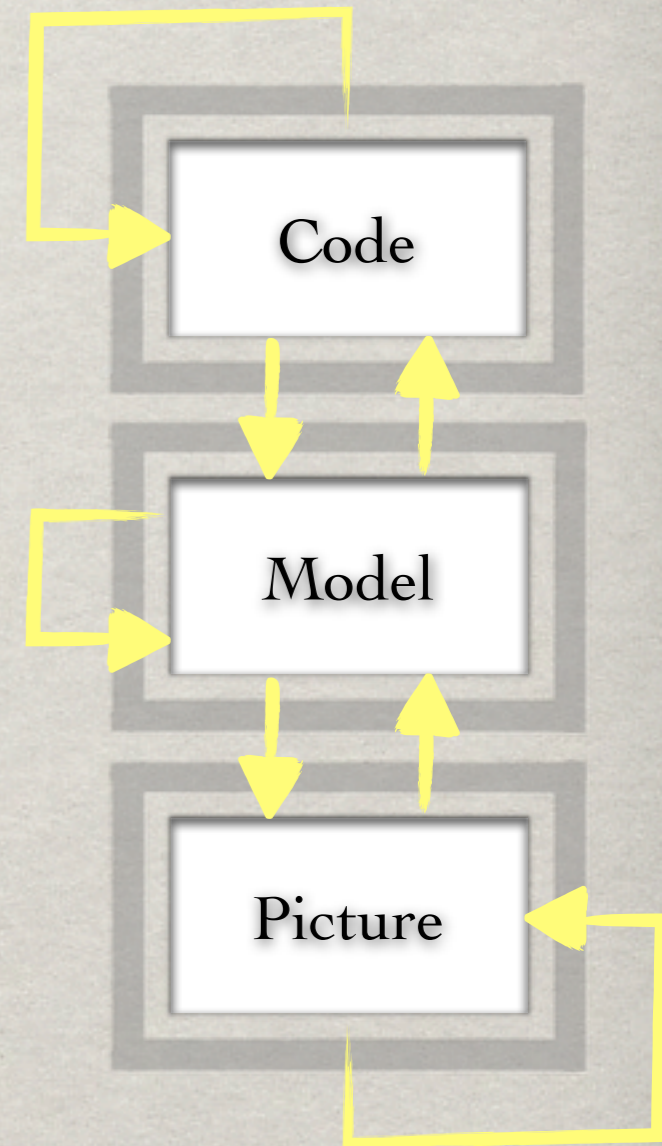
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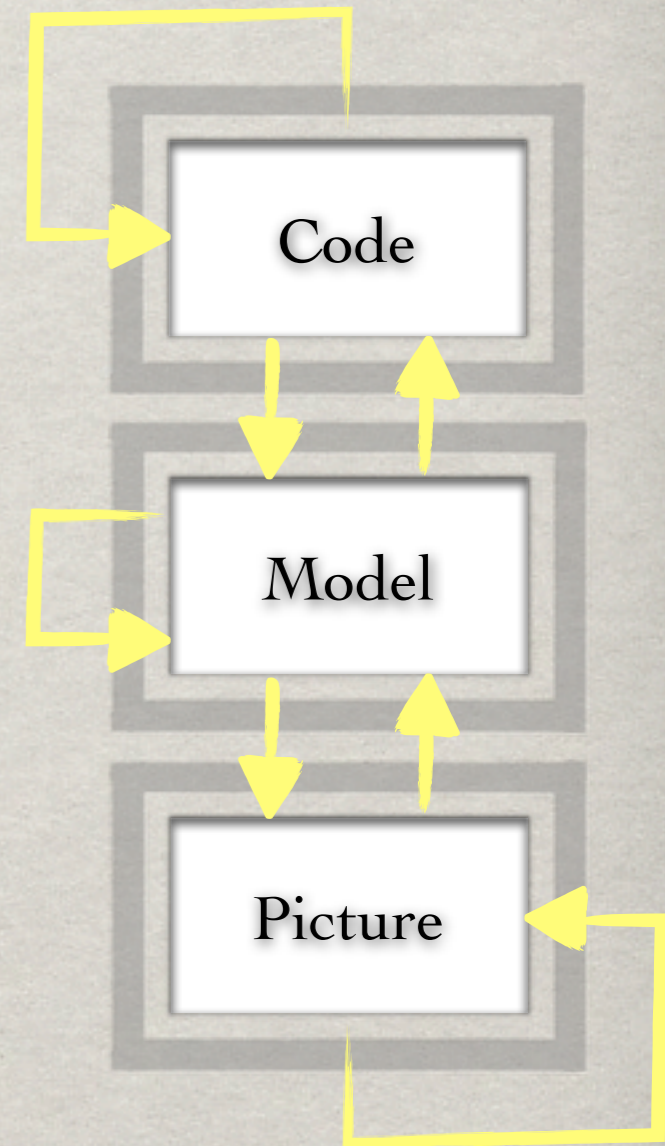


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- ✱ SYnthesize
 - ✱ Generate code
 - ✱ Transform code
 - ✱ Produce data and visuals

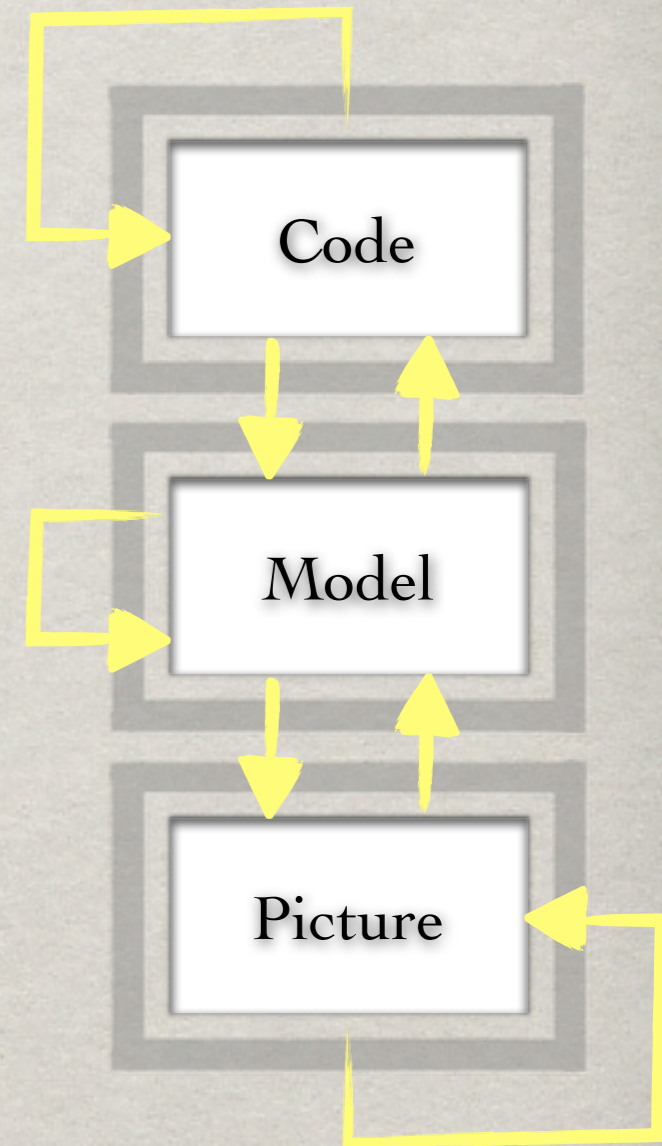


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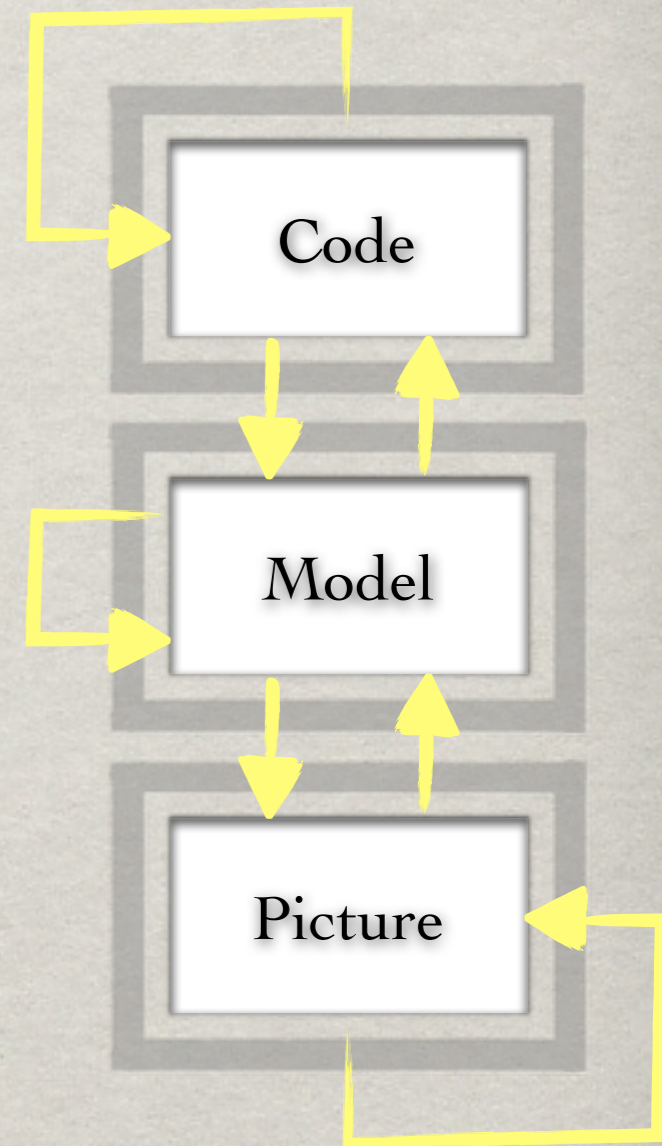
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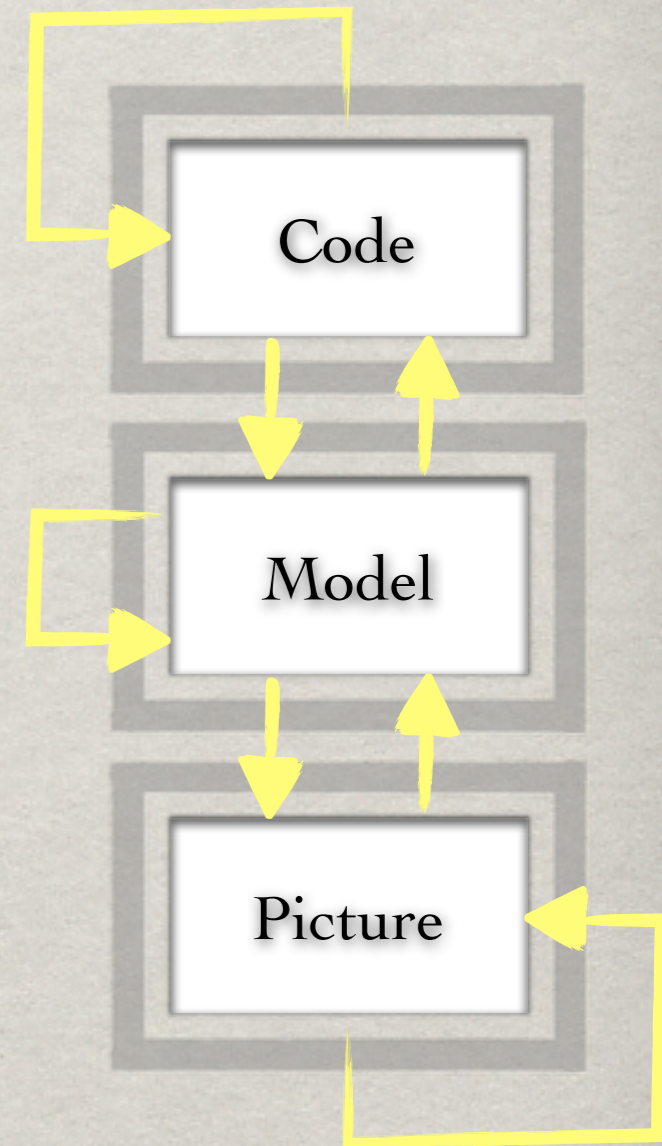
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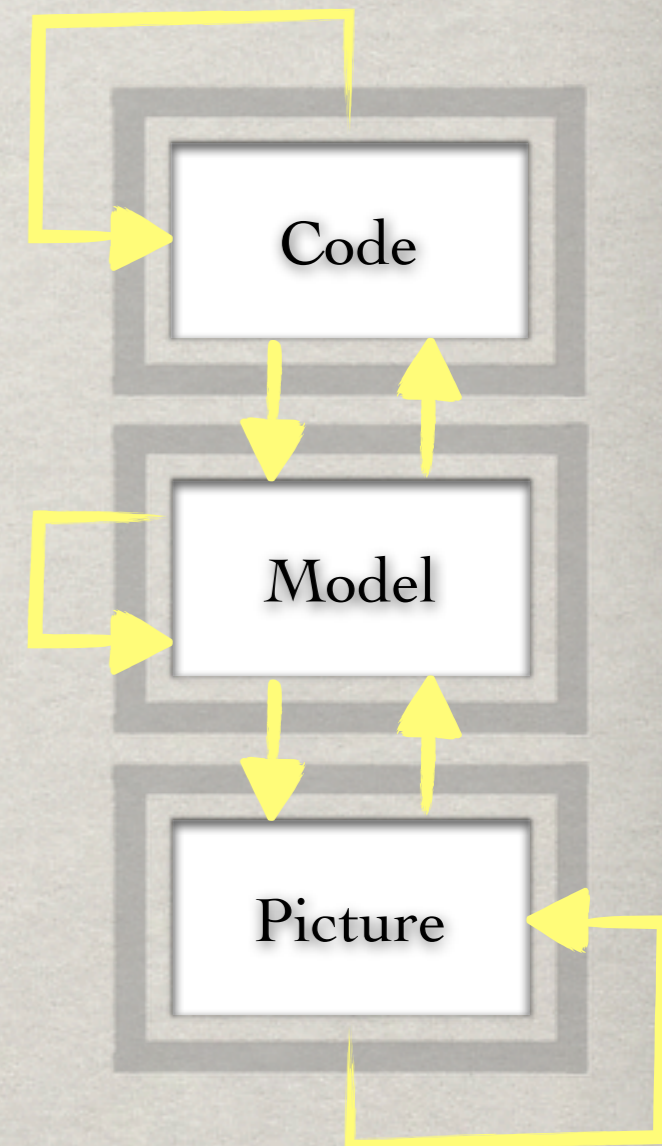


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- ✱ SYnthesize
 - ✱ String templates
 - ✱ Concrete syntax
 - ✱ Interactive visualization generator



A ONE-SLIDE DSL




```
metro {
```

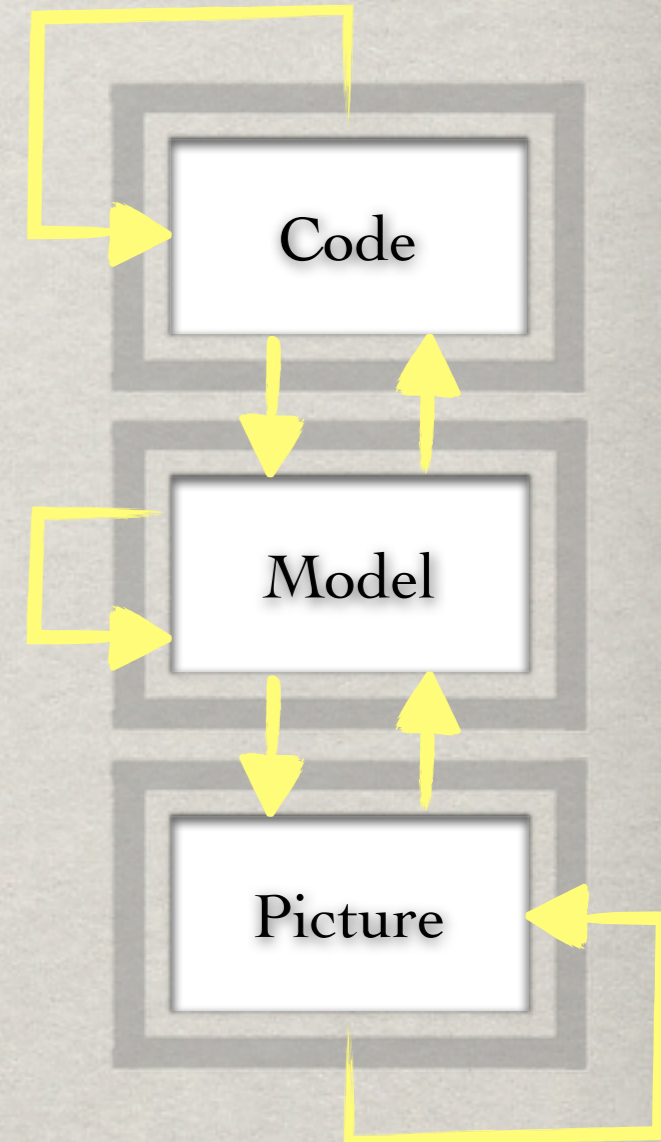
```
  Centraal Waterloo Weesperplein Wibautstraat Amstel;
```

```
  Amstel Spaklerweg Overamstel Rai Zuid;
```

```
  Rai Zuid Amstelveenseweg Lelylaan Sloterdijk;
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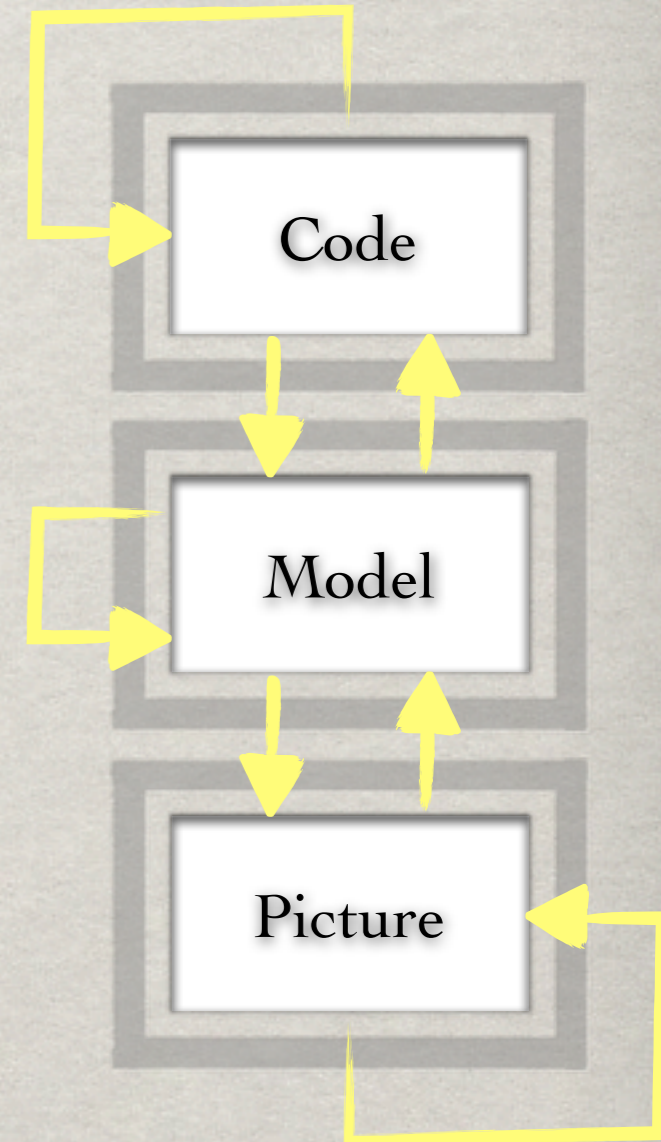
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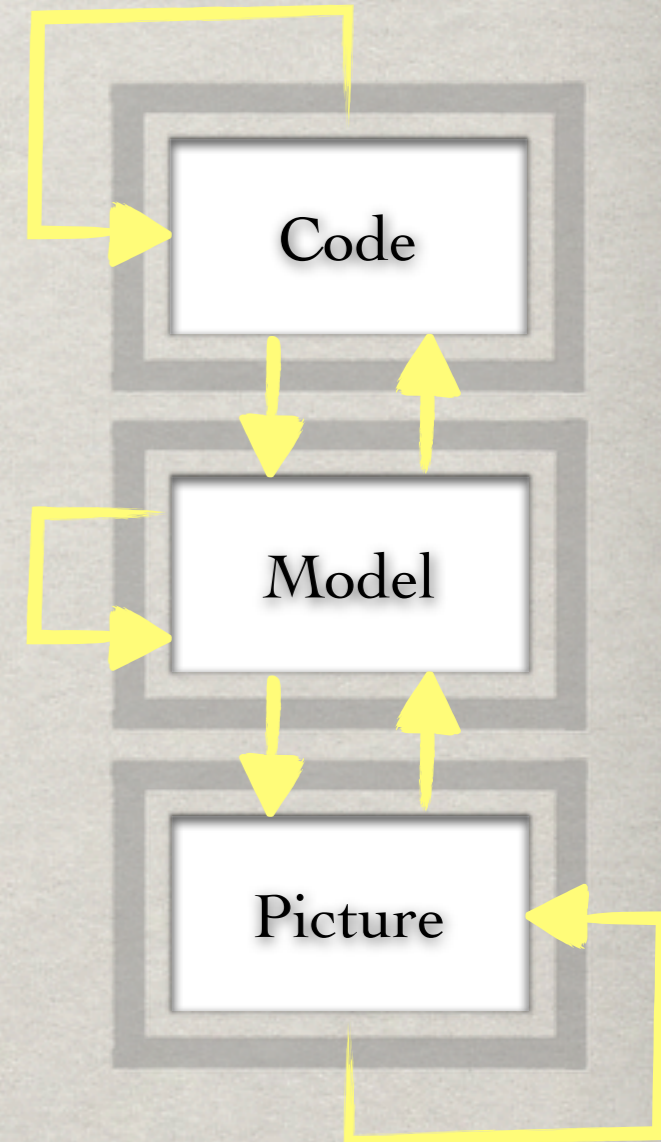
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```

```
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```

```
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```



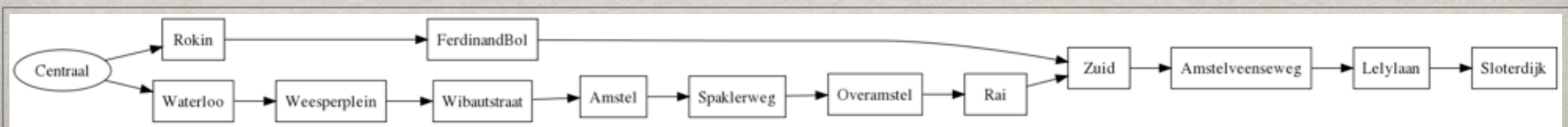
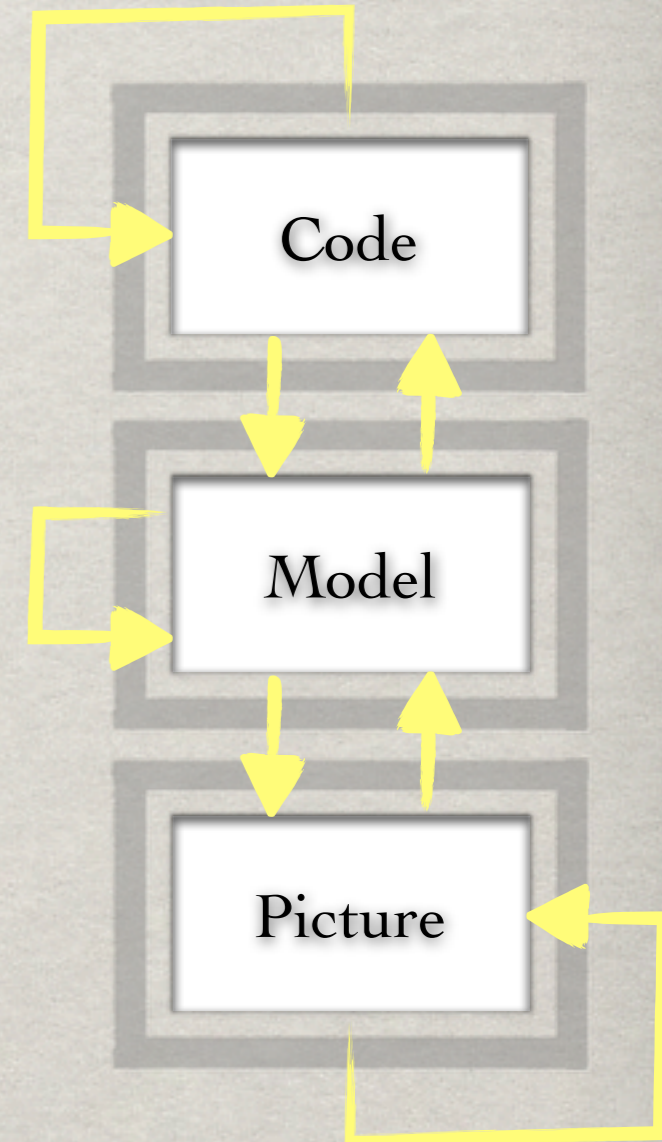
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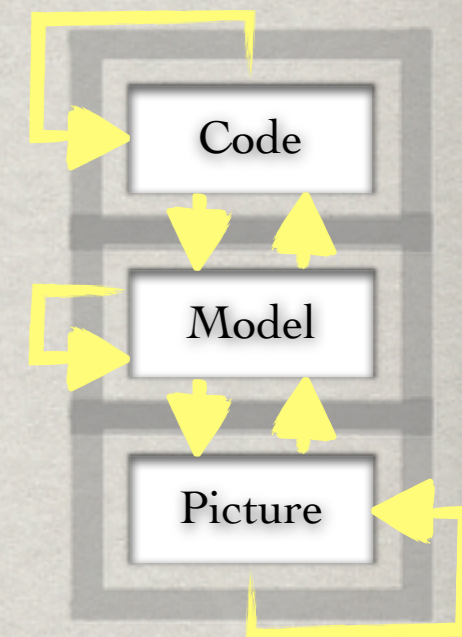
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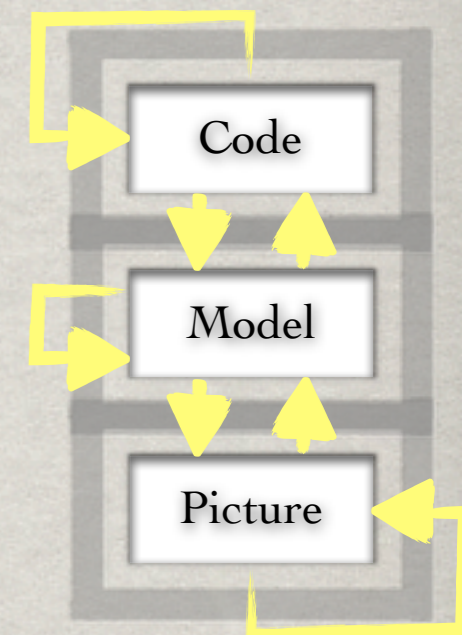
```
digraph Metro {
  node [shape=box]
  Centraal -> Waterloo
  Waterloo -> Weesperplein ...
  Centraal [shape=ellipse]
}
```



A ONE-SLIDE DSL

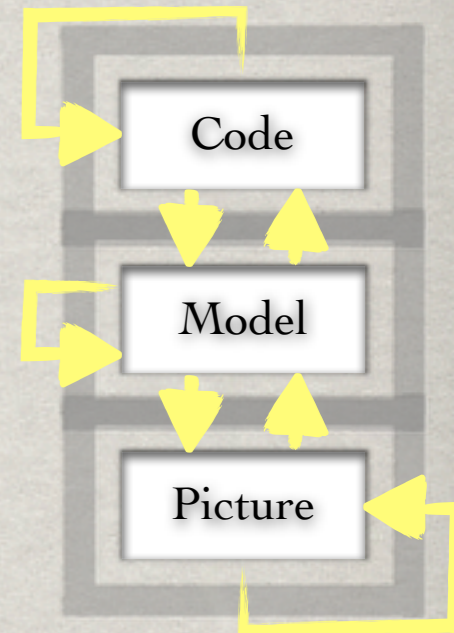


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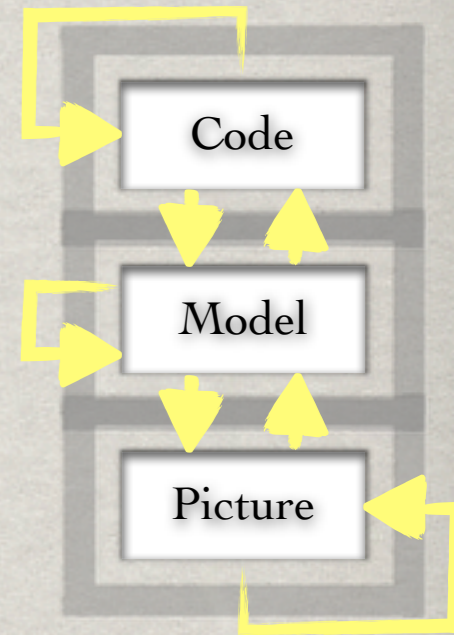
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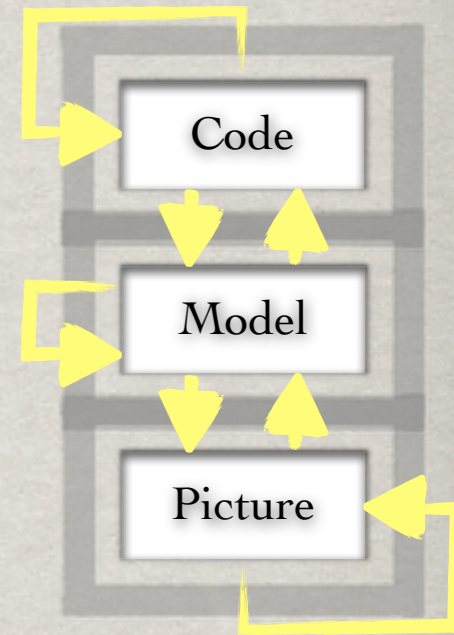
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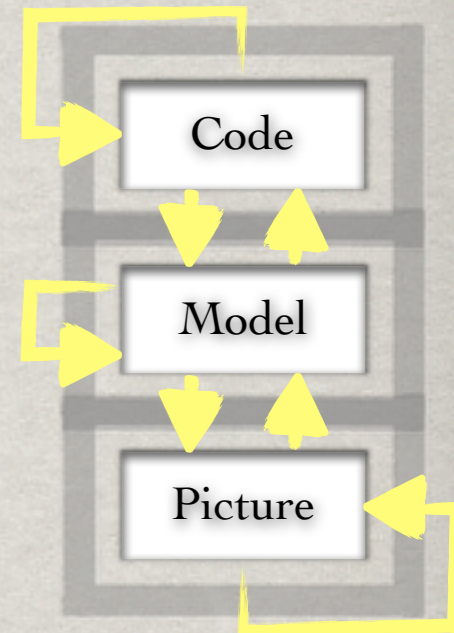
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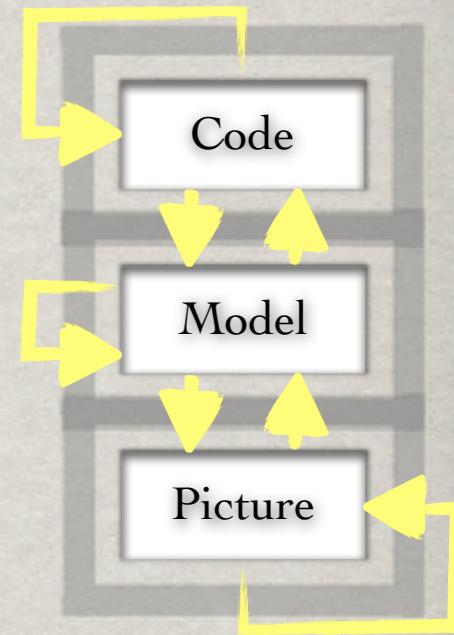


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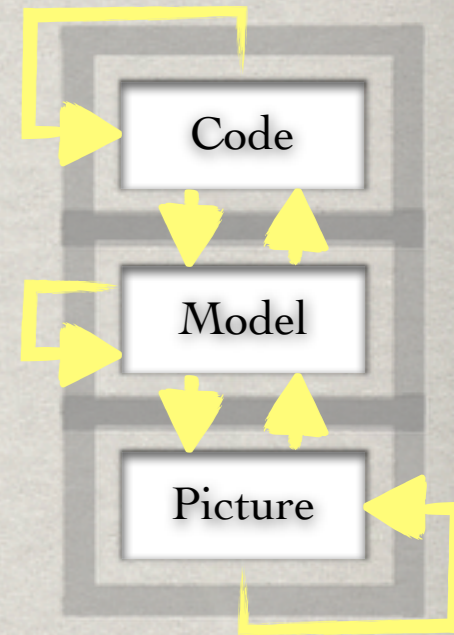
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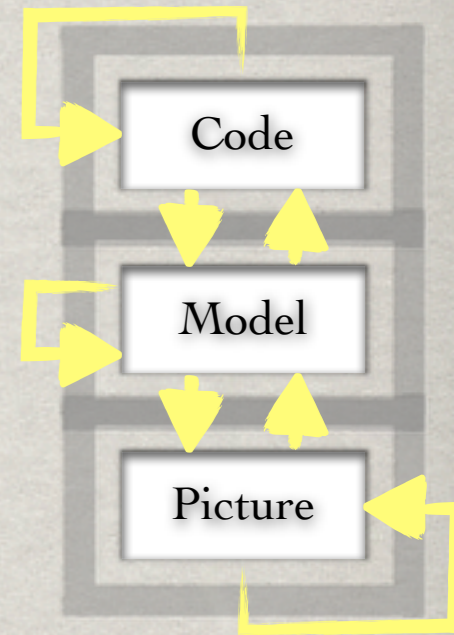
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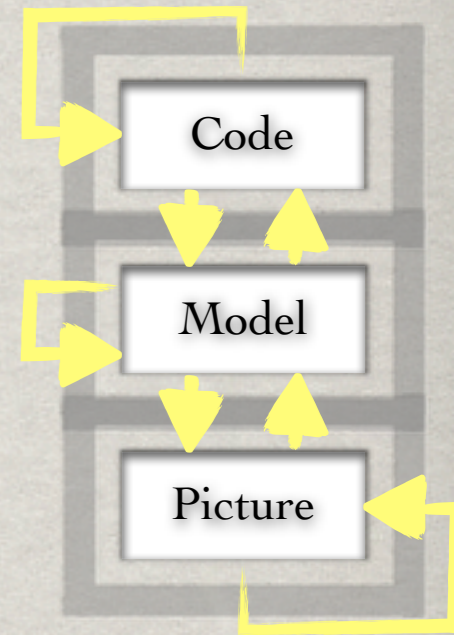
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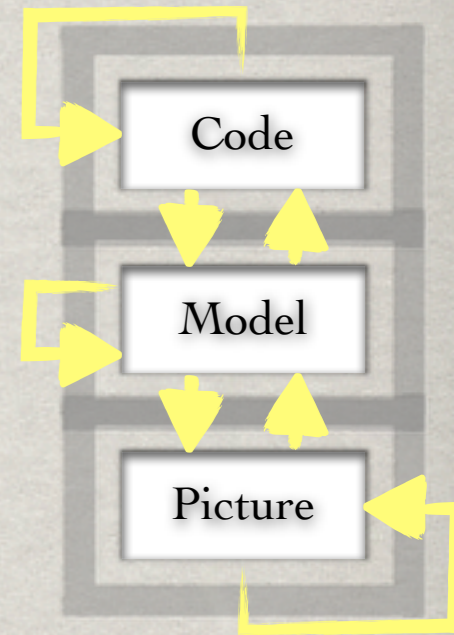


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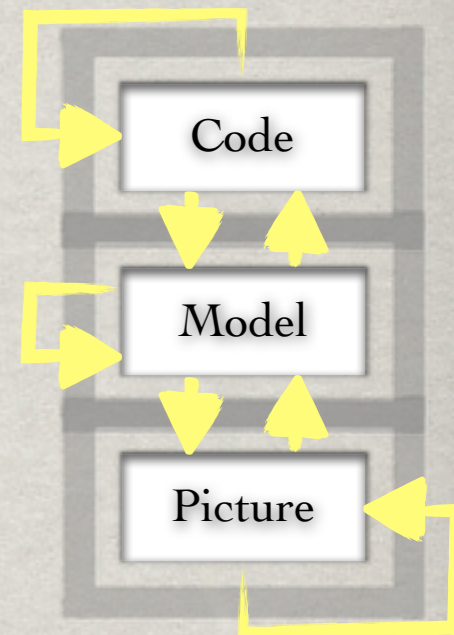


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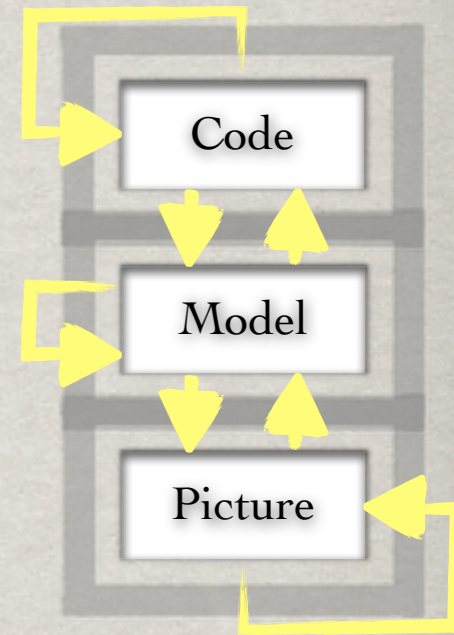



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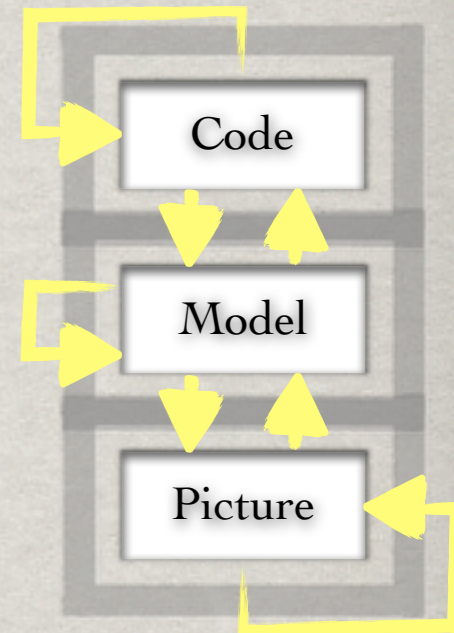
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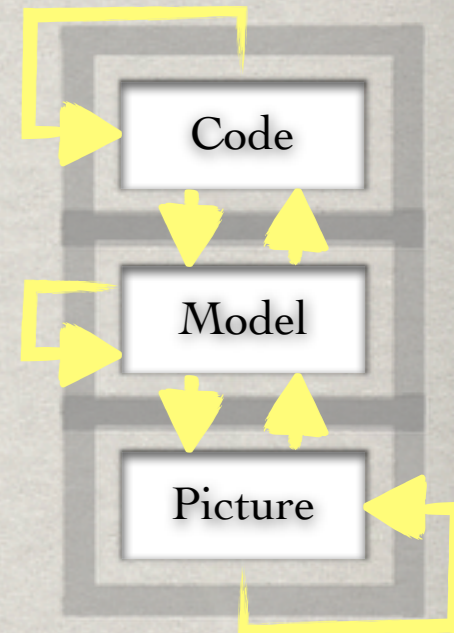
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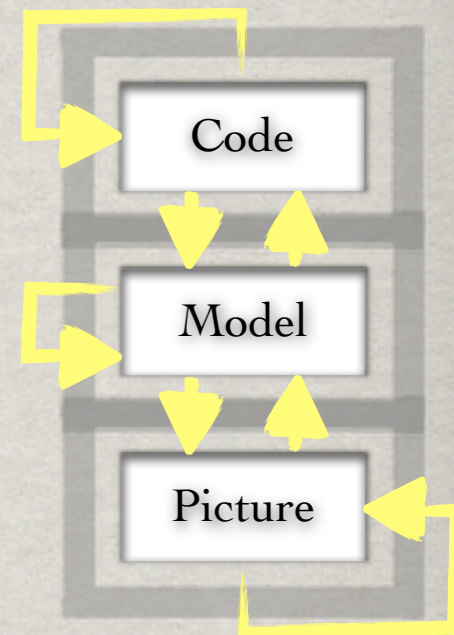
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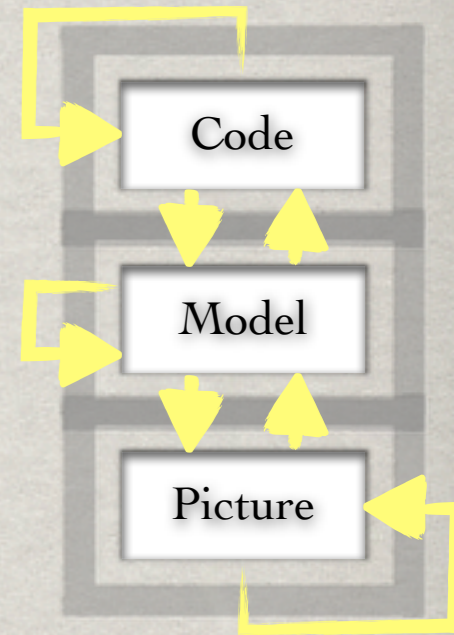
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bool isHub(rel[Id,Id] metro, Id station) = size(metro[Id]) > 1;

void synthesizeDotGraph(loc target, rel[Id from,Id to] metro) {
  writeFile(target,"digraph Metro { node [shape=box]
    '<for (<from, to> <- metro) {>
    ' <from> -\> <to><}>
    '<for (st <- metro<from>, isHub(metro, st)) {>
    ' <st> [shape=ellipse]<}>
    '}&quot;);

```



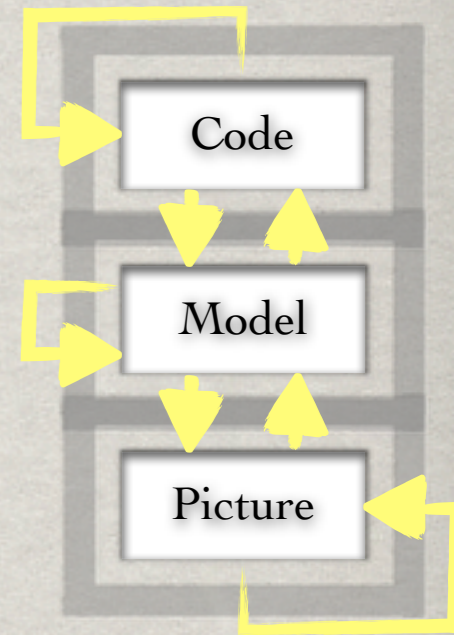
```

module Metro
start syntax System = "metro" "{" Track* tracks "}";
syntax Track = Id+ stations ";" ;
lexical Id = [A-Za-z][A-Za-z0-9]*;
layout WS = [\ \t\n\r]*;

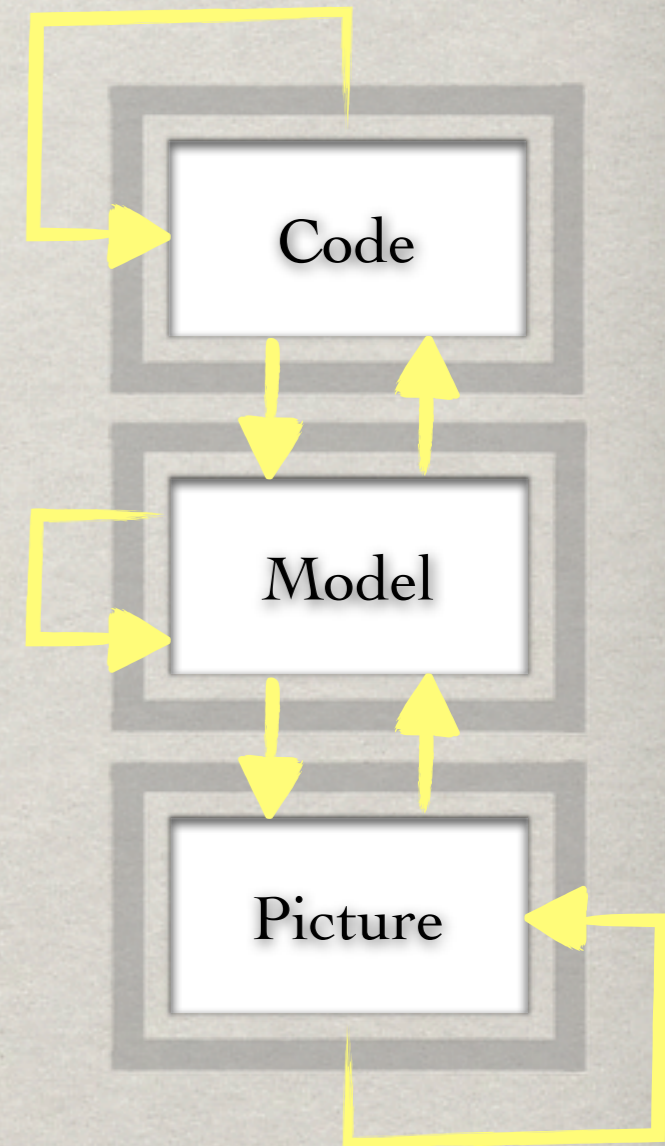
rel[Id,Id] extractMetroGraph(loc source) =
  {<from, to> | /Track t := parse(#start[System], source),
    (Track) `bool isHub(rel[Id,Id] metro, Id station) = size(metro[Id]) > 1;

void synthesizeDotGraph(loc target, rel[Id from,Id to] metro) {
  writeFile(target,"digraph Metro { node [shape=box]
    '<for (<from, to> <- metro) {>'
    ' <from> -\> <to><}>'
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    '}&quot;);

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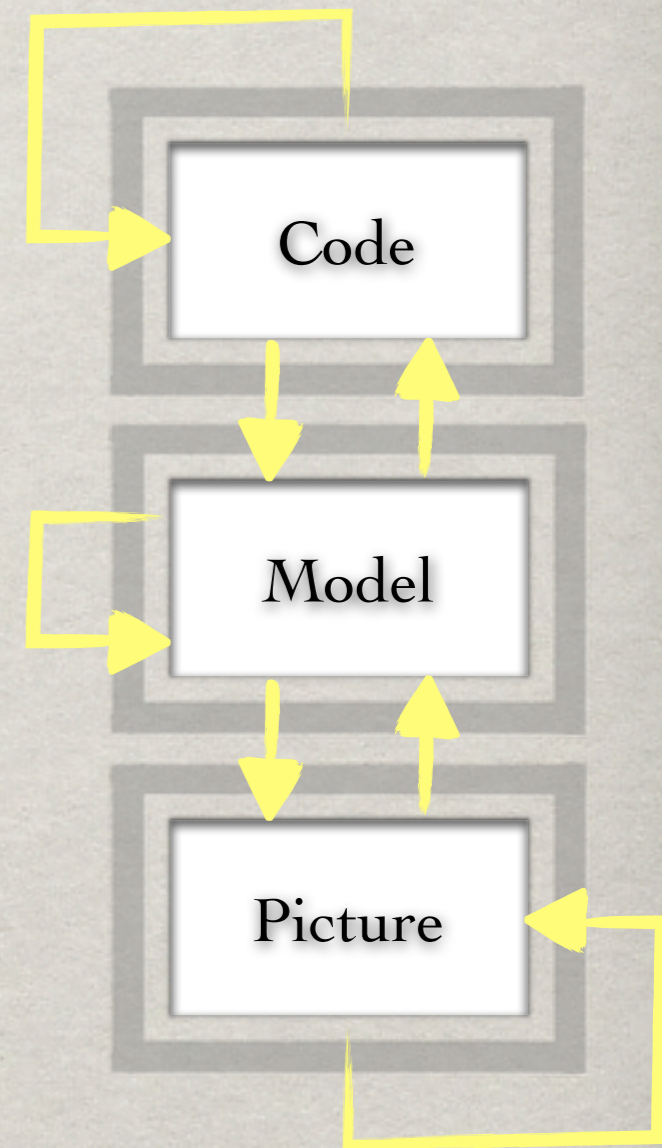


A ONE-SLIDE DSL



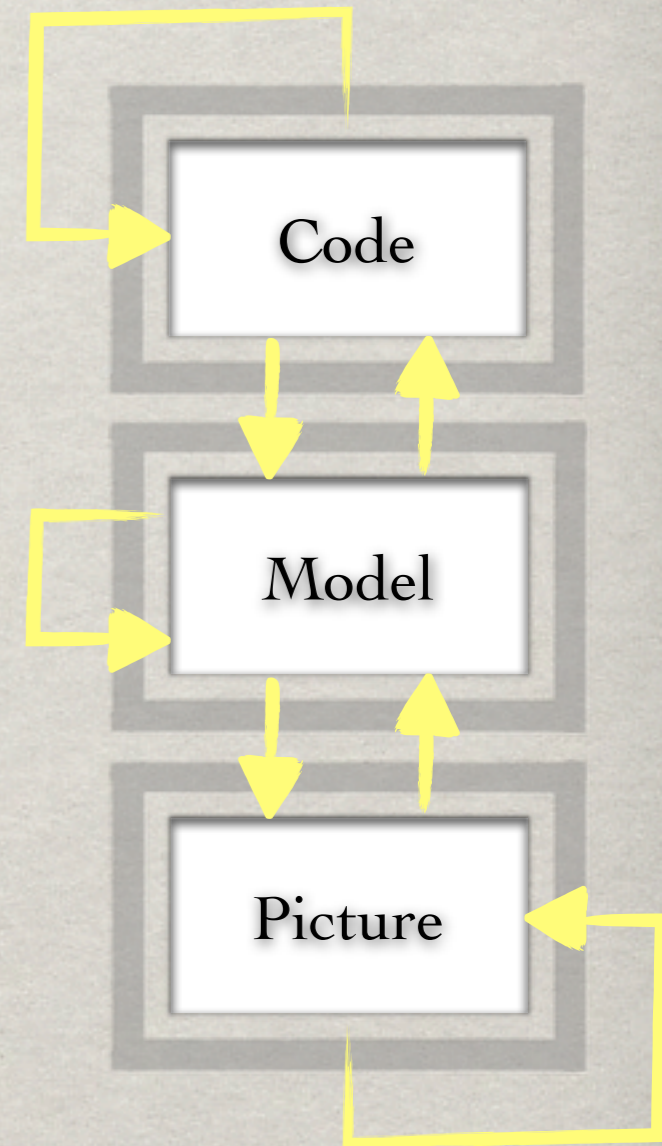
A ONE-SLIDE DSL

- ✿ What is the point?
 - ✿ Rapid tool development
 - ✿ No boilerplate
 - ✿ No glue
 - ✿ No magic
 - ✿ Done. Next!

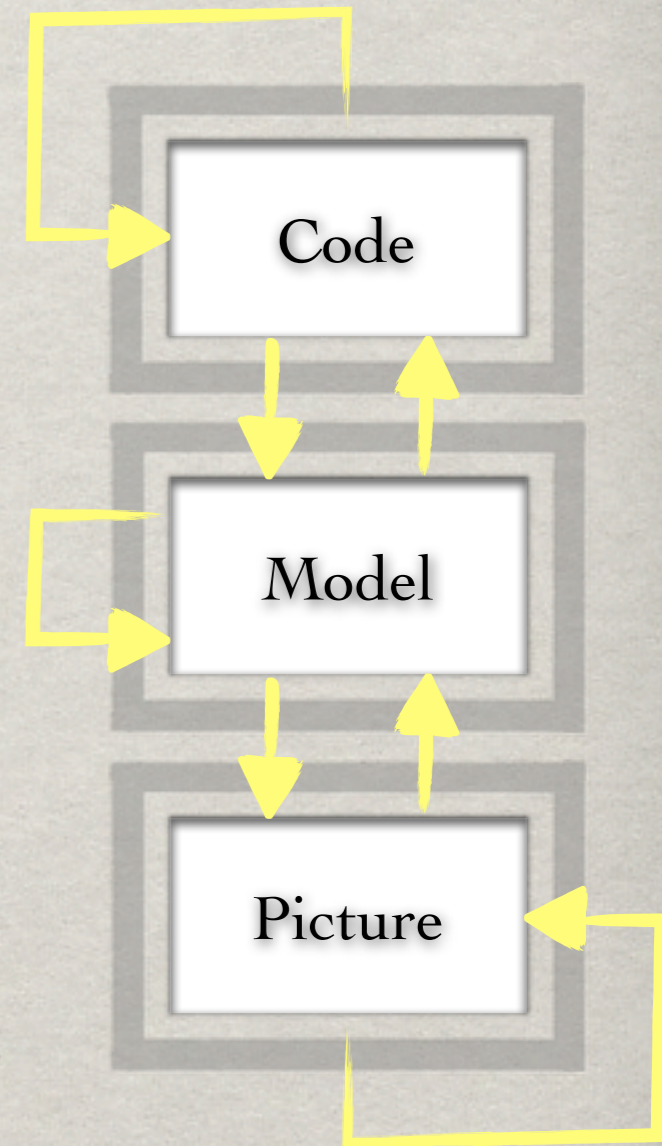


A ONE-SLIDE DSL

- ✱ What is the point?
 - ✱ Rapid tool development
 - ✱ No boilerplate
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 - ✱ Done. Next!
- ✱ This works for
 - ✱ all kinds of meta-programming tools
 - ✱ all kinds of languages

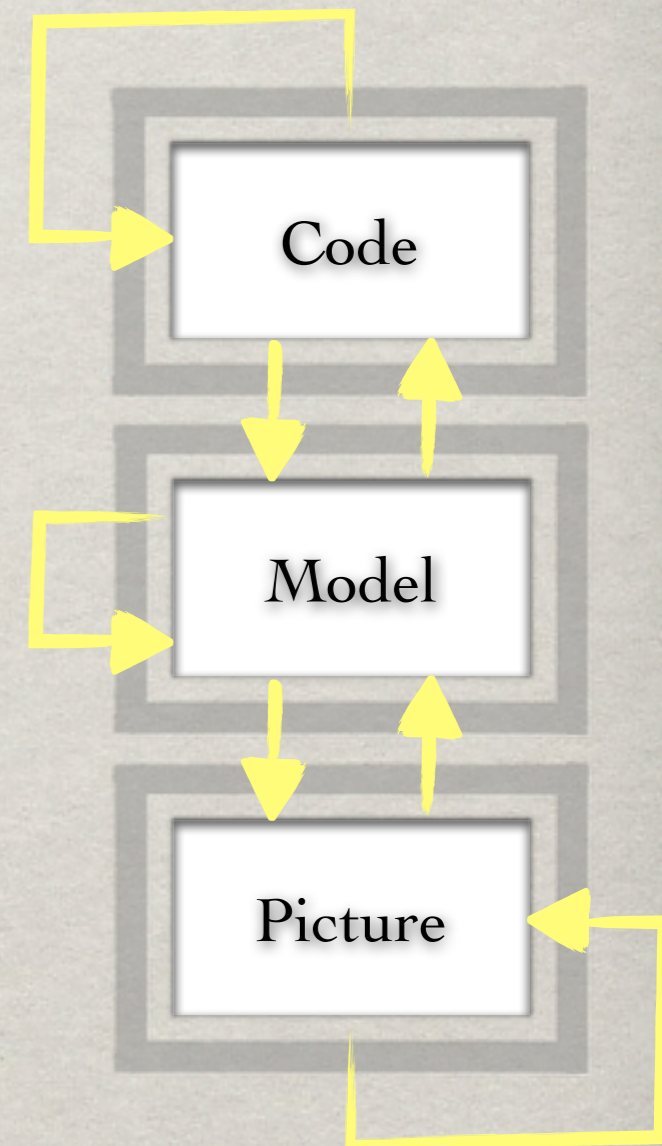


LIBRARY DEVELOPMENT



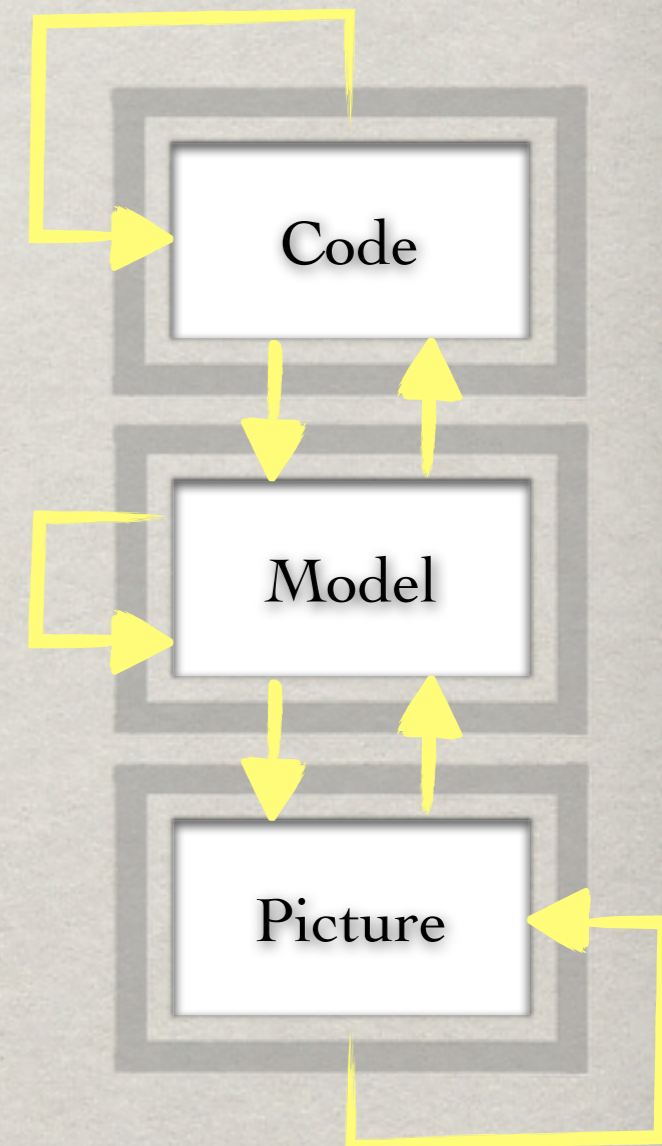
LIBRARY DEVELOPMENT

- ☀ **Type-safe** access to resources such as bug databases, version management systems, spreadsheets, webservices



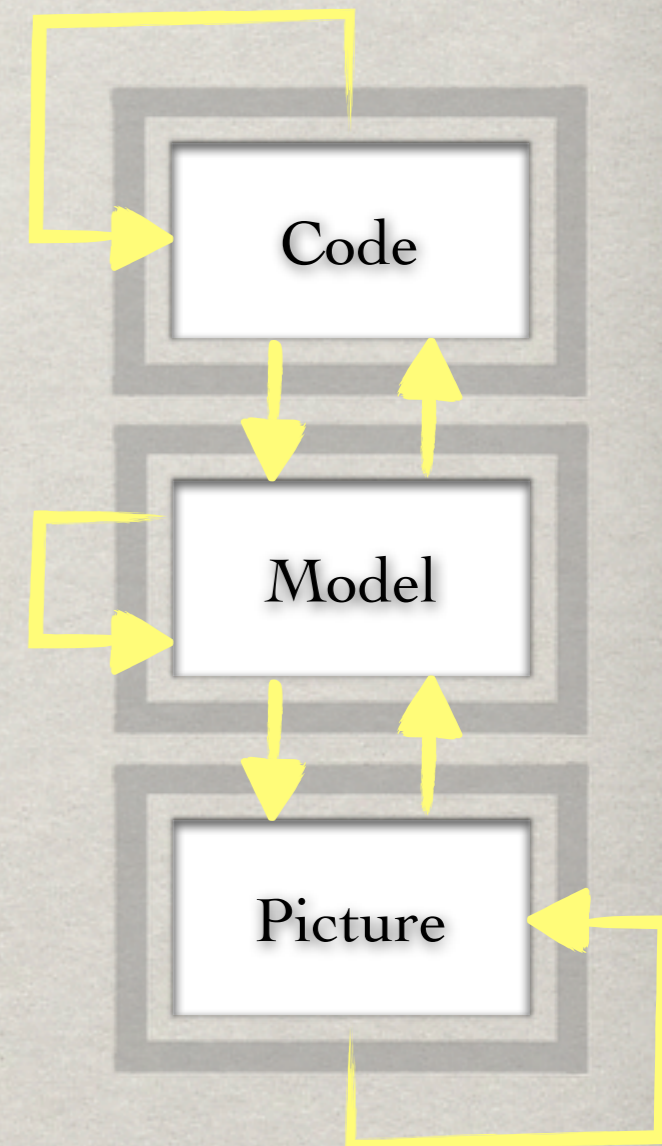
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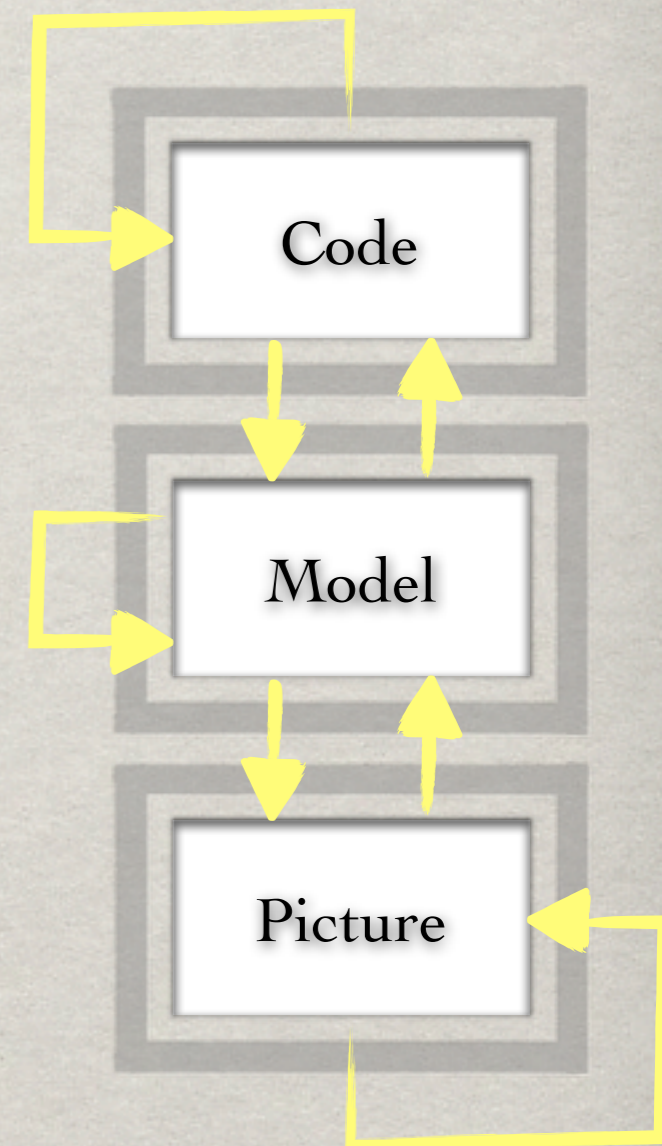


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- ☀ **Front-ends** for programming languages
- ☀ **Generic analyses**; statistics, constraints, satisfiability, ...
- ☀ **Visualization**: one-stop-library for any visualization (graph, chart, browser, ...)



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STATE OF THE ART

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