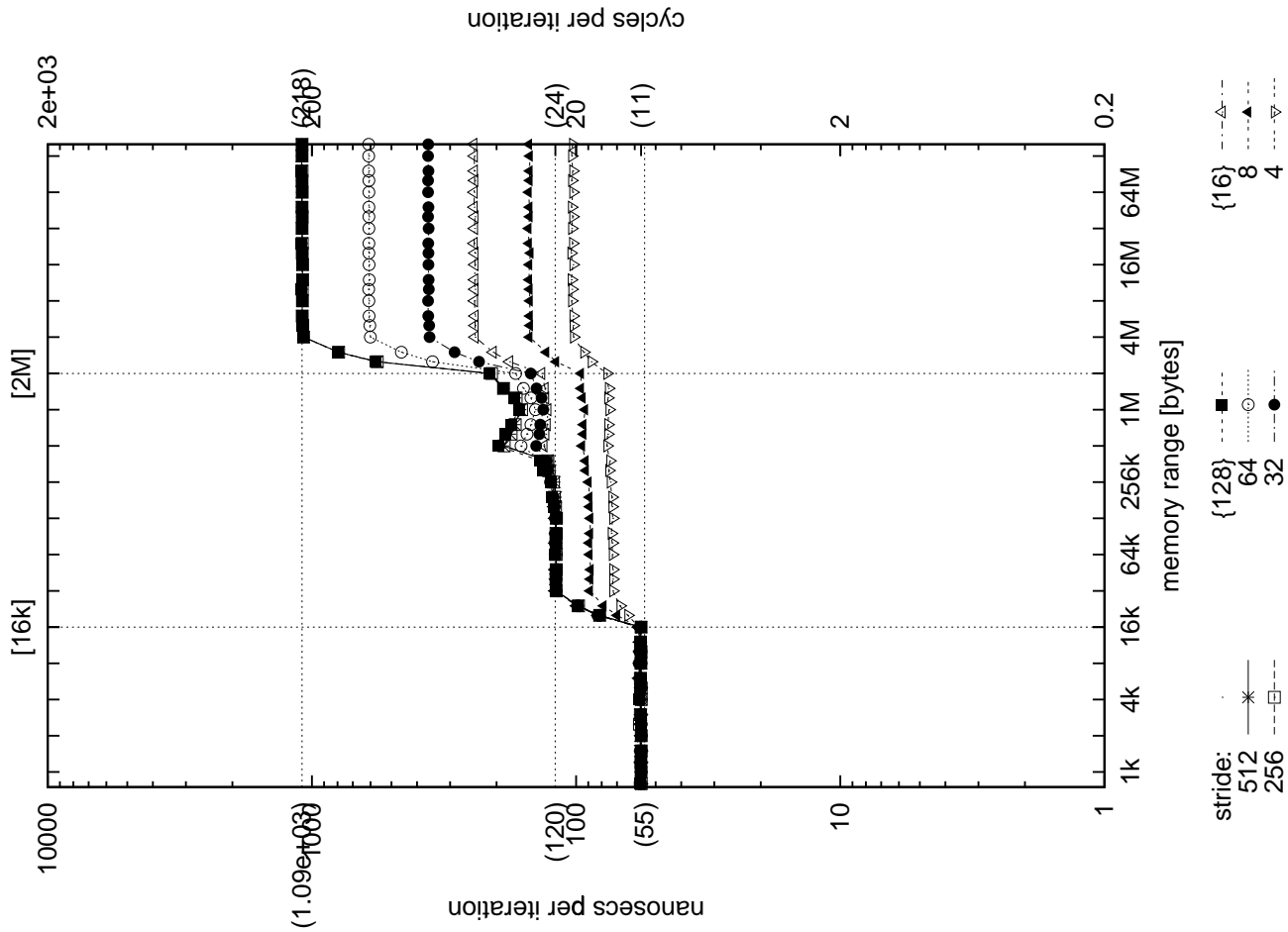


le103-200MHz-128MB.cache



le103-200MHz-128MB.TLB

