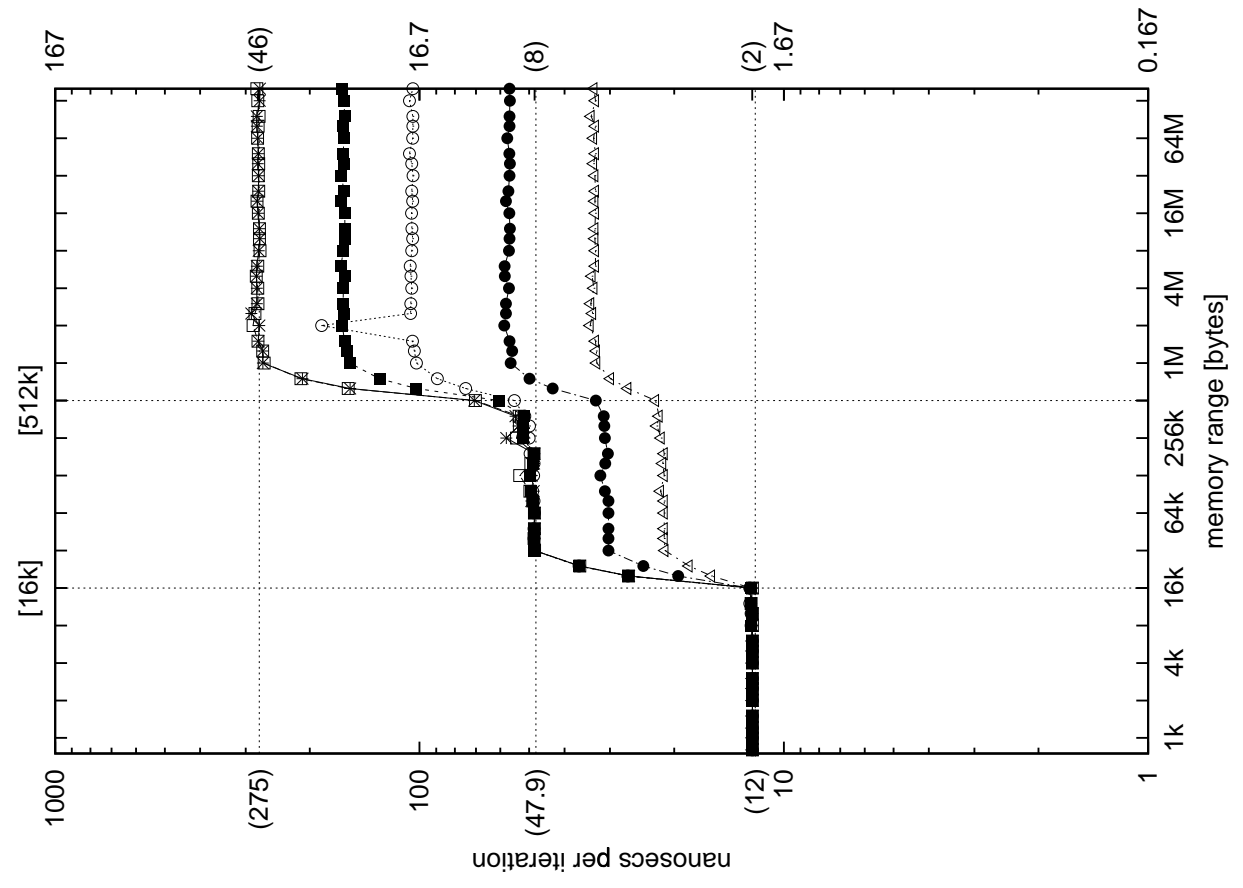
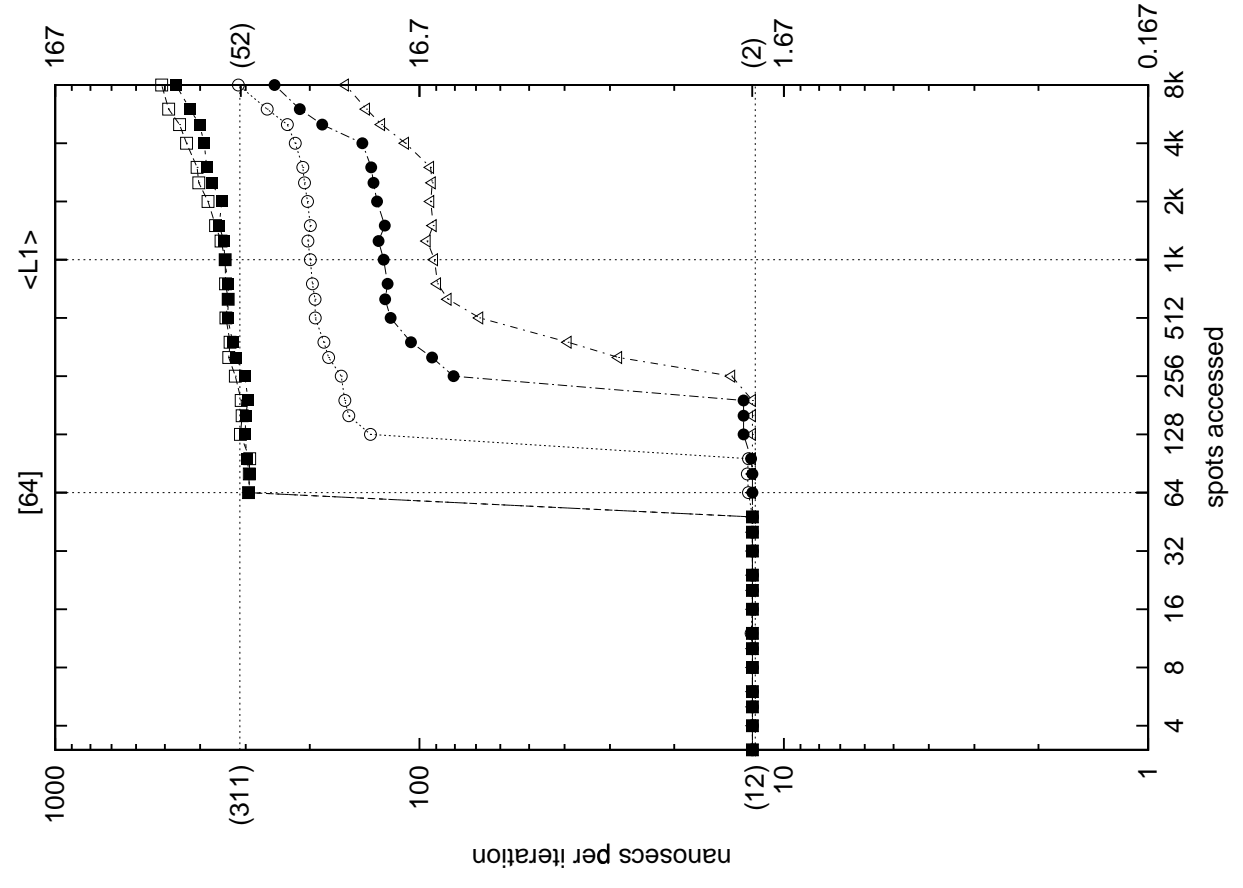


le106-167MHz-128MB.cache



stride: 128 \* {64} {32} {16} 8 4

le106-167MHz-128MB.TLB



stride: 16448 {8256} 4160